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Indium tin oxide thin film preparation and property relationship for humidity sensing: A review

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Abstract

This review aims to present a critical overview of indium tin oxide (ITO) thin film preparation methods, structure–property relationship, and its humidity sensing. A range of passive and active humidity sensors with thin films (based on metal oxides) detects humidity. ITO thin film has advantageous properties, such as low resistivity and high stability, making it highly suitable for humidity sensing applications. ITO thin film has shown the efficient level of humidity sensing, and a compatible size of humidity sensor can monitor the interface conditions humidity. So far, the application of ITO thin film for humidity measurement has yet to be explored at commercial scale, specifically in the detection of lower environmental humidity range (below 5% relative humidity (RH)). The research reveals a gap in improving the ITO thin film properties with an optimal range of preparation conditions. The research opportunities in the preparation, properties, characteristics, and efficient humidity sensitivity of ITO thin film are reviewed in this work.

KEYWORDS

annealing temperature, humidity sensing, indium tin oxide (ITO), thickness, thin film

1 | INTRODUCTION

Indium tin oxide (ITO) is a transparent semiconducting material with the advantages of electrical conductivity, low resistivity, and high optical transmittance in the visible region, along with a wide bandgap ranging from 3.5 to 4.3 eV, and with good chemical stability. ITO has various applications such as antistatic applications, architectural coatings, transparent electrodes in solar cells and flat panel displays, gas sensors, organic light-emitting diodes (OLED), and humidity sensing. There are several methods to prepare ITO thin films, each with specific advantages and limitations. ITO thin film properties are directly related to the preparation conditions. During preparation, mainly it depends on the annealing and substrate temperature, tin concentration, and thin film thickness. Research on ITO thin film humidity measurement has been ongoing for over a decade. A critical evaluation reveals that there is still an opportunity to enhance ITO thin film properties and its humidity sensing efficiency.

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ITO thin films have been prepared using various methods, such as dip coating method,^{1–7} DC magnetron sputtering,^{8–14} radio frequency (RF) sputtering,^{15–24} spin coating,^{25–34} direct and thermal evaporation,^{35–37} electron beam (EB) evaporation,^{38–42} pulsed laser deposition (PLD),^{43–45} chemical vapor deposition,^{46,47} microwave heating,^{48,49} screen-printed process,^{50–56} and spray pyrolysis method.^{57–59}

Critical assessment of the methodologies indicates that ITO thin film properties are highly dependent on the multiple preparation processes conditions such as substrate temperature, deposition rate, substrate pre-preparation, tin concentration, oxygen partial pressure, and annealing temperature, which are discussed below in much detail.^{11,15} Arguably, the crystalline structure of ITO has a significant influence on ITO thin film properties and performance. Typically, a stabilizer is used which is an additive to preserve the crystalline structure, and a binder used which is an agent to enhance the adhesion of the material. For example, Dong et al.²⁷ proposed a new approach of using oxalic acid as a stabilizer, methylcellulose as binder, and post thermal treatment at 500°C applied to improve the conductivity and transmittance of ITO thin film. Analysis indicated that the thin film resistivity decreases through an annealing process. Additionally, the low resistivity of the thin film could be generated by increased oxygen vacancies and substitutional tin dopants on indium.^{59,60} It has been suggested that the deposition rate plays a critical role in determining the ITO thin film quality, and the resistivity of thin films linearly increases with the deposition rate. A low deposition rate promotes crystalline phase formation,⁶¹ whereas a high deposition rate by the sputtering method causes thermal damage to the substrate. As an example, Hoshi et al.⁶² used sputtering to minimize the substrate's thermal damage at a high deposition rate. ITO is a highly promising material for high-temperature thin film sensor applications. ITO thin films, when coated on alumina substrates, underwent thermal cycle tests from 500°C to 1200°C. After the thermal tests, the morphology results showed a smoother surface with no cracks or pinholes. Additionally, recrystallization of these films improved the average grain size.⁶³

Various active and passive humidity sensors are available to monitor humidity in a range of applications such as agriculture, predictive maintenance, instrumentation, healthcare, automation, climate monitoring, food-quality preservation, and domestic applications (Figure 1).^{64–67} To meet the demands for humidity measurements, it required the further development of cost-effective, high-performance sensors, the establishment of innovative sensing modes, process technologies, and incorporation of new materials to provide cheaper, a wide dynamic range, and quick response time.⁶⁵ The development of advanced ITO thin films with high humidity sensing properties is highly necessary for various industries, particularly for effective monitoring of interface conditions and low humidity levels. Compared to other humidity sensing materials, ITO thin film can offer better humidity sensing with its advantageous properties. The preparation of advanced ITO thin films and compact sensor design could provide an opportunity for economically effective humidity monitoring. Various metal oxides such as graphene oxide (GO), zinc oxide (ZnO), zirconium dioxide (ZrO₂), barium oxide (BaO), and tin oxide (SnO₂) have been considered for different humidity sensing applications. GO is a highly promising humidity sensing material and showed excellent physical properties of mechanical stiffness (130 GPa), thermal conductivity (4.84×10^3 W mK⁻¹ to 5.30×10^3 W mK⁻¹), large surface specific area (2600 m² g⁻¹), carrier mobility (200,000 cm² V⁻¹), bandgap (2.2 eV), and electrical and optical properties. Inkjet printing deposited GO layer could perform with a quick response (2.7 s) and recovery (4.6 s) time with a humidity range of 11%–97%.^{68,69} An advanced form of the ZnO (in the nanostructure form) has shown high-level humidity sensing properties, a bandgap of 3.37 eV, and high thermal and mechanical stability at room temperatures.^{70,71}

ZrO₂ is an ideal ceramic material with various practical applications such as catalysis, as optical devices, as fuel cell electrolytes, including humidity sensing.⁷² Hydrothermal synthesis is a unique method to synthesize ZrO₂ nanoparticles with high purity and controlled microstructures, which could be a better option to enhance the properties. The ZrO₂ samples with high surface proportions provides high oxygen absorption and high humidity sensitivity.⁷³ Agool, Kadhim and Hashim⁷⁴ studied nanocomposites of polyvinyl alcohol-polyethylene glycol-polyvinyl pyrrolidone (PVA-PEG-PVP) blend with ZrO₂ nanoparticles as an additive for humidity sensing. ZrO₂ (weight: 0%, 2%, 4%, 6%, 8%) mixed with the PVA (90 wt.%), PEG (5 wt.%), and PVP (5 wt.%). Analysis showed that the resistance decreases with increasing relative humidity for all nanocomposites thin films. The absence of ZrO₂ nanoparticle thin film showed a higher resistance with relative humidity at room temperature. Sathya⁷⁵ designed the humidity sensor thin film of Al₂O₃ on the ZrO₂ and measured the humidity from 10% to 90% RH with an average sensitivity of 2.226.

Barium oxide (BaO) has a unique wide energy bandgap (3.8 eV) compared to other metal oxides, and the humidity sensing performance of BaO nanoparticles (measured through DC resistance method) with relative humidity (RH)

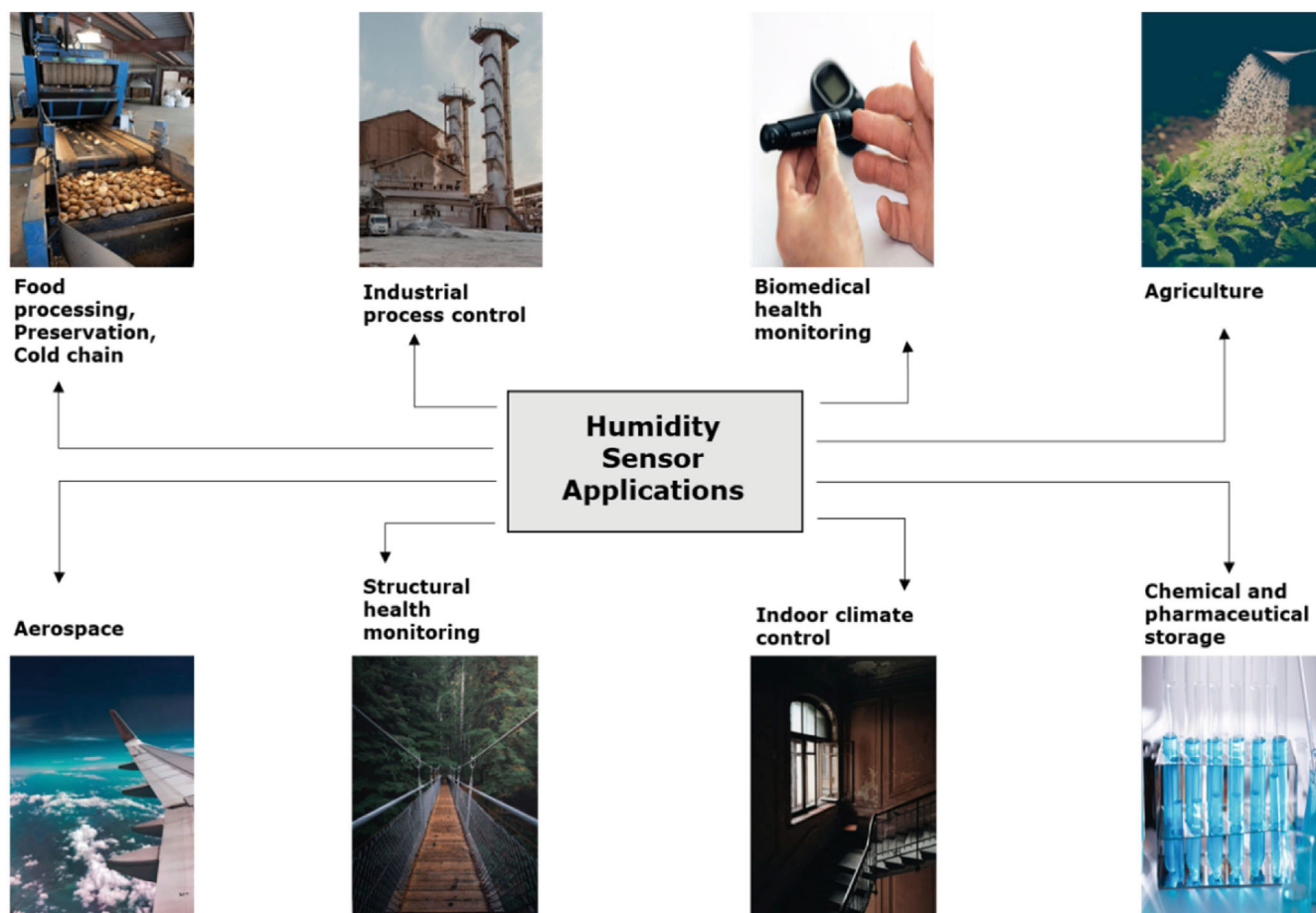


FIGURE 1 Diverse application of humidity sensors (representative examples) (permissions not needed for these images, attribution is not required).

ranging from 5% to 98% at room temperature. It showed linear resistance behavior with relative humidity. The result confirmed the quick response and recovery time of the 40 and 110 s, respectively.⁷⁶ SnO₂ is a rutile structure and n-type semiconducting material with a bandgap of 3.6 eV at 300 K, with good electrical and optical properties, and high chemical stability.⁷⁷ Parthibavarman, Hariharan and Sekar⁷⁸ deposited SnO₂ nanoparticles using microwave irradiation technique. The relative humidity raised from 5% to 95%, increasing the conductivity and reducing the resistance, and it showed the response and recovery time of 32 and 25 s, respectively. Li, Li and Zhang⁷⁹ designed a high-performance humidity sensor (zinc oxide/tin oxide (ZnO/SnO₂)) with low-cost metal oxide of tin oxide by using the solvothermal method. SnO₂ enhanced the humidity absorption, and ZnO improved the oxygen-rich vacancies on the composite thin film, allowing for the absorption of the water molecules and speeding up the ionization, increasing the response and recovery time performance. ZnO/SnO₂ humidity sensor showed the 35 and 8 s of response and recovery time in the humidity range of 11%–95%. Each humidity sensing materials has advantageous properties and limitations for humidity sensing. These limitations can be overcome through further development in preparation and sensor design.

This review has attempted to critically evaluate ITO thin film preparation methods, the effect of the processing conditions, structure–property relationships, and humidity sensing. Previous literature studies covered the ITO thin film humidity sensing of humidity ranging from 5% RH to 98% RH, including those yet to explore below 5% RH level measurement. An appropriate condition for humidity measurement of ITO thin film preparation still needs to be discovered. Assessing the impact of such materials, fabricating an improved ITO thin film with humidity sensing applications, and improving sensitivity from low to high relative humidity can prevent humidity-related accidents and economic losses in industries.

2 | ITO THIN FILM PREPARATION

Various preparation methods are available to prepare ITO thin film based on each method's unique advantages and limitations. For example, thin film resistivity and transmittance changes based on the preparation methods such as EB evaporation ($3.0 \times 10^{-6} \Omega\text{m}$, 92%), reactive evaporation ($7.0 \times 10^{-6} \Omega\text{m}$, 80%), PLD ($4.0 \times 10^{-6} \Omega\text{m}$, 85%), thermal deposition ($9.1 \times 10^{-6} \Omega\text{m}$, 80%), and DC magnetron sputtering ($4.0 \times 10^{-5} \Omega\text{m}$, 97%).³⁹ Based on published literature, Figure 2 presents ITO thin film preparation methods (various approaches). The process steps involved in the synthesis of ITO thin film preparation highly depend on preparation conditions and the substrate used. The synthesis process is important for determining the quality and characteristics of the ITO thin film. The relationship between the concentration of tin, deposition method, substrate, and annealing temperature with the characterization of the ITO thin film is reviewed below. Particularly, the selection of the substrate depends on the application, deposition method, and temperature levels. Generally, the substrates are cleaned ultrasonically in acetone, rinsed in water, and dried in an oven before deposition begins. The solutions are prepared through different procedures based on the deposition method, with continuous stirring for a certain duration, followed by the addition of other agents (i.e., capping, precipitate agent) to enhance solution quality, bonding between the substrate and thin film, and extend the service life of the thin film.

Nishio Sei and Tsuchiya⁸⁰ demonstrated the successful deposition of an ITO thin film on a quartz glass substrate using a dip coating process. The substrate was directly dipped into an ITO solution, and then heat treatment was carried to enhance crystallization during the dip coating process. Though still challenging, advantages of the dip-coating process included its cost-effectiveness and the ability to deposit multiple layers through repeated heating and dipping processes. The stability and sensitivity of dip-coated thin films were suitable for sensing applications of gas and humidity.^{4,5} Sputtering has been frequently used to deposit the ITO thin film to obtain high conductivity at low substrate temperature deposition. The dip-coating fabrication could be used to deposit ITO thin film on a large area at a low cost, although the film's conductivity may generally be lower than that achieved through sputtering.³

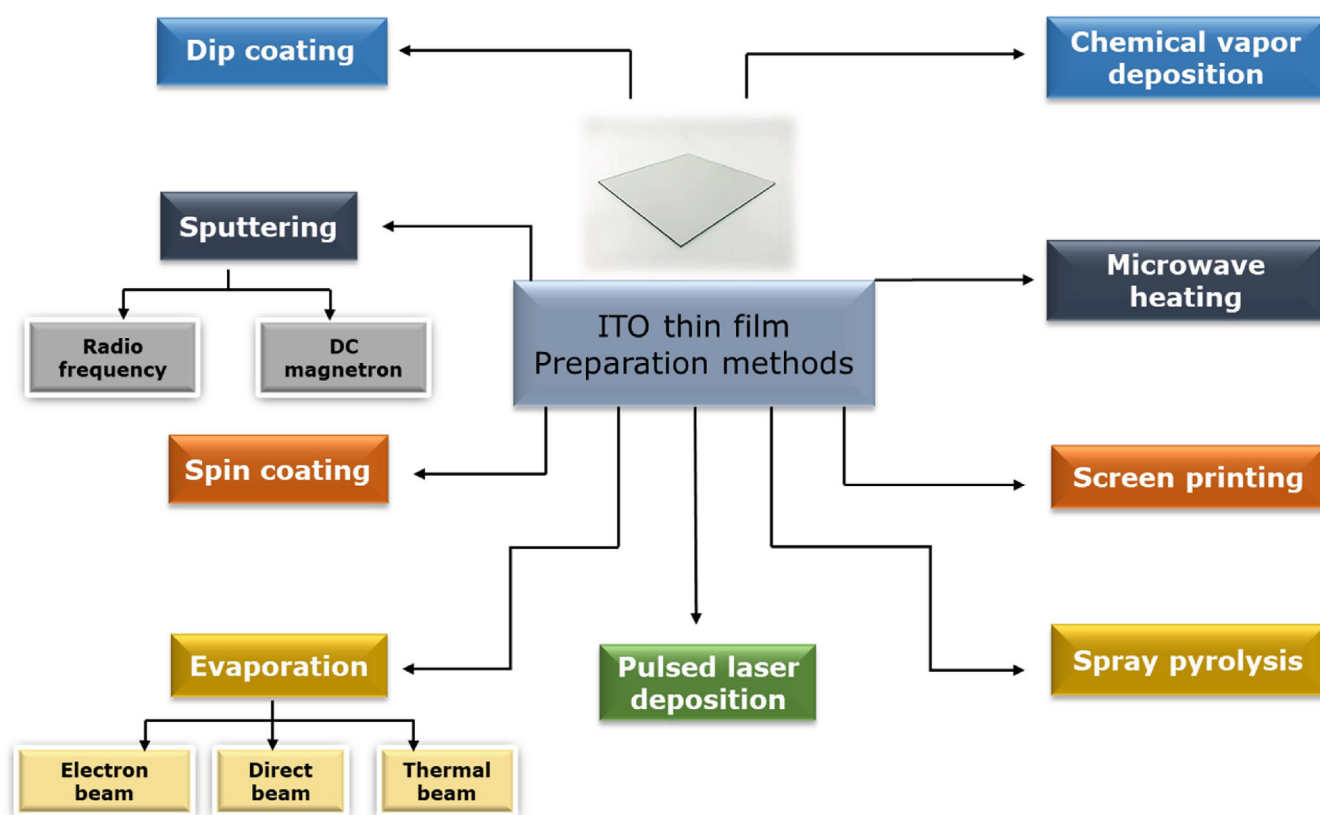


FIGURE 2 Types of ITO thin film fabrication methods (authors original images).

Many years ago, Almeida⁸¹ discussed the fundamental physics and design of DC sputtering. In recent times, ITO thin films have been fabricated by DC magnetron sputtering, starting from room temperature to 400°C on to a glass substrate.^{9,10} However, DC magnetron sputtering of conductive oxide material has been frequently accompanied by an electric arc as a form of electric breakdown, and the electric breakdown mostly appeared on the target substrate. This issue can be overcome by using a pulsed DC magnetron sputtering system.⁸ El Akkad et al.¹⁷ investigated the deposition of ITO thin films by radio frequency (RF) sputtering method. RF sputtering allows thin film deposition from room temperature to 230°C, with varying deposition rates.²¹ Compared to the other available deposition methods, DC magnetron sputtering is commonly used method for good film preparation and has been available for large industrial production.¹¹ However, it requires high-cost vacuum equipment, which is considered a disadvantage. Another disadvantage of sputtering is a high surface roughness at room temperature deposition. Thin films with high surface roughness are generally unsuitable for many applications, especially humidity sensing, as high surface roughness can reduce humidity measurement sensitivity,⁵ primarily due to reduced contact area, and slower response time due to trapped air in small crevices.

Kim et al.²⁹ considered preparing ITO thin film on a glass substrate at room temperature by sol-gel spin coating. It was demonstrated that a repeated process could be beneficial to make a multilayer deposition, with decreased surface roughness and film resistance.⁸² Various methods have been applied to prepare spin coating solution with different concentration levels. Spin coating is a beneficial method for thin film preparation with the required shape and size. In such method, the doping level and concentration of a solution could be controlled easily. Moreover, it is cost-effective and yields stable, uniform, and robust films with better reproducibility by a simple process. The uniformity of thin film is confirmed by SEM images as demonstrated by Babu and Vadivel.²⁵ Sofi, Shah, and Asokan³⁵ fabricated ITO thin films on glass substrate by thermal evaporation technique, followed by oxidation for 15 min in atmosphere in temperature range of 600°C–700°C. Followed by surface morphology analysis carried out by using SEM. The results showed that, thin film is uniform and good adherence with glass substrate without any defects like cracks, voids, and pinholes within the grains. With these advantages, Singh et al.⁸³ synthesized nanomaterial ITO thin film layers on a glass substrate by adding a dilute solution to the substrate, which spun at 3000 rpm for 60 s. The results showed advanced materials characterization of ITO thin film via spin coating, supported by the presence of indium doped SnO₂ nanomaterial.

Multiple types of evaporation techniques, such as thermal, direct and electron evaporation, have been applied to deposit ITO thin film on glass substrates. George and Menon³⁹ considered ITO thin film deposition by the EB evaporation technique. Patel, Makhija and Panchal⁸⁴ fabricated the ITO thin film on the alumina substrate through thermal evaporation. Evaporation techniques are known for their effectiveness in producing high-quality thin films. In these techniques, deposition control is achieved by adjusting the oxygen pressure during the process.^{40,41} It is worth noting that ITO thin films prepared using evaporation methods typically require high temperatures, which can be considered one of their limitations.³⁶ Kim et al.⁸⁵ grew the ITO thin film on a glass substrate by PLD. Craciun et al.⁴⁴ deposited a high-quality ITO thin film at temperature range from 40°C to 180°C on glass and silicon substrate using ultraviolet-assisted pulsed laser deposition (UVPLD). The average thickness of these films was 150 nm. However, the same thickness of thin film (i.e., 150 nm) can be achieved through PLD at a temperature of 300°C.

Maruyama and Fukui⁴⁷ prepared ITO thin film on a glass substrate by chemical vapor deposition (CVD) in an electric furnace without using an oxygen donor. Atmospheric pressure chemical vapor deposition (AP-CVD) offered a simple process to do deposition on a large area compared to the other methods. It did not require any specific vacuum condition or coating chamber to do a process, resulting the cost lower for thin film production.⁴⁶

Microwave heating is a unique way to produce thin film with low resistance and high optical transparency in few minutes. Rapid thermal annealing (RTA) is a well-known process to reduce mechanical damage and diffuse impurities within thin film compared to long time annealing and heat-treatment processes. Microwave heating operates similar to RTA. In such process, the prepared ITO solution can be applied and dried at 80°C on to a glass substrate. The glass plate can then undergo quick microwave irradiation. The microwave irradiation at 2.5 GHz was conducted using a commercial microwave with a power output of 700 W. Through such microwave heating process, ITO thin films can be deposited 5 times repeatedly on a glass substrate to achieve thickness of 900 nm. The advanced approach of microwave enhanced reactive magnetron sputtering could help to avoid oxidation of the sample during sputtering.^{48,49}

Screen print has an advantage that it could be printed on selected or specific areas. Bessais, Ezzaouia and Bennaceur⁵¹ prepared the ITO thin film on glass and silicon substrate by screen printing technique. McGhee et al.^{12,50} printed ITO on the PET substrate, which was cured in the oven at 105°C before applying alumina ink on top. Subsequently, the final ITO thin film was printed on top of the alumina ink and cured for 5 min at 105°C. The analysis demonstrated that the alumina dielectric ink was effective in preventing short circuits between the two ITO layers. Siddiqui, Saxena and Singh⁸⁶ performed screen printing to deposit silver bus bars on the edges of glass substrate and followed by the deposition of ITO through sputtering on remaining areas.

Benamar et al.⁸⁷ investigated the electrical, structural, and optical properties of ITO thin films prepared by spray pyrolysis on a glass substrate. ITO thin films were deposited on the glass substrate by spray pyrolysis with different solution concentration levels at a substrate temperature of 500°C. It was demonstrated that the film's adherence depends on the substrate, transparency, and pinhole free. Marikkannu et al.⁵⁸ prepared ITO thin films by the JNS pyrolysis technique on heated glass and porous silicon substrate at a 350°C–450°C temperature. The air pressure was 3.5 kg cm⁻², the distance of the nozzle from the substrate was 5 cm, and rate of spray was 0.75 mL min⁻¹. The spray continued for 10 mins on both substrates.

Some recent study highlights the advantages of using a glass substrate as the base structure. ITO thin films on polyethylene terephthalate (PET) and glass substrates have been investigated for the CO₂ reduction reaction in water vapor. According to the manufacturer's information, the thickness of the ITO thin film on the glass substrate ranged from 30 nm to 60 nm, while the PET substrate of 130 nm thickness. Analysis revealed that the ITO glass substrate exhibited a lower peak intensity compared to the ITO-PET substrate. This lower intensity indicates more efficient electron/hole separation on the ITO glass substrate compared to the ITO-PET substrate, suggesting a higher number of charge carriers in the ITO glass. The XRD pattern of the ITO glass substrate showed a peak corresponding to the cubic In₂O₃ structure, whereas the XRD pattern of the ITO-PET substrate did not exhibit the ITO structure.⁸⁸

In summary, the polymer substrate had the advantages of being lightweight and having higher shock resistance. However, the high temperature of thin film preparation and heat treatment can lead to the polymer substrate decomposition, releasing toxic compounds. It is important to note that structural degradation can introduce microcracks in ITO thin film.¹⁹ Overall, it has been demonstrated that the glass substrate is more appropriate for ITO thin film fabrication compared to the polymer substrate due to the glass substrate can withstand higher temperatures without any structural decomposition and failure. Overall, various literature demonstrated that the spin coating method has the advantage of low cost, efficiently controlled doping level, simplicity, and flexibility in preparing large and small sizes of thin films. In recommendation, spin coating is more appropriate to prepare ITO thin film in lab scale. The preparation conditions relationship with thin film properties is discussed below.

3 | ITO THIN FILM PROPERTIES AND PREPARATION DEPENDENCIES

Based on a critical review of ITO thin film preparation methods, along with their challenges and advantages, it has become evident that ITO thin film properties are highly dependent on various thin film preparation conditions, particularly including annealing and substrate temperature, tin concentration, and thin film thickness. Thin film's parameters such as resistivity, surface roughness, grain size, crystalline structure, bandgap, and transmittance are interconnected with preparation conditions. The relationship between preparation conditions and thin film parameters are critically reviewed here. An improved ITO thin film properties can be achieved through selection of appropriate preparation conditions. Figure 3 illustrates the thin film properties' connection with the fabrication parameters. Modifications in preparation conditions directly influence thin film properties and humidity sensing behavior, as discussed below.

3.1 | Annealing temperature

The annealing process is essential for enhancing resistivity behavior, transmittance, bandgap, carrier concentration, carrier mobility, and grain size of ITO thin films. Typically, the annealing temperature range from 200°C to 700°C, and annealing time duration depends on the applications of the thin film. An annealing process promotes the transformation from amorphous to a polycrystalline structure, with oxygen vacancies enhancing conductivity.

Ota et al.³ deposited ITO thin film by dip coating on a glass substrate (Corning #7059, 25 mm × 37 mm × 0.7 mm). The coated substrate was annealed at 600°C for 1 h with a heating rate of 10°C min⁻¹ and N₂-0.1% H₂ flow rate. As a result

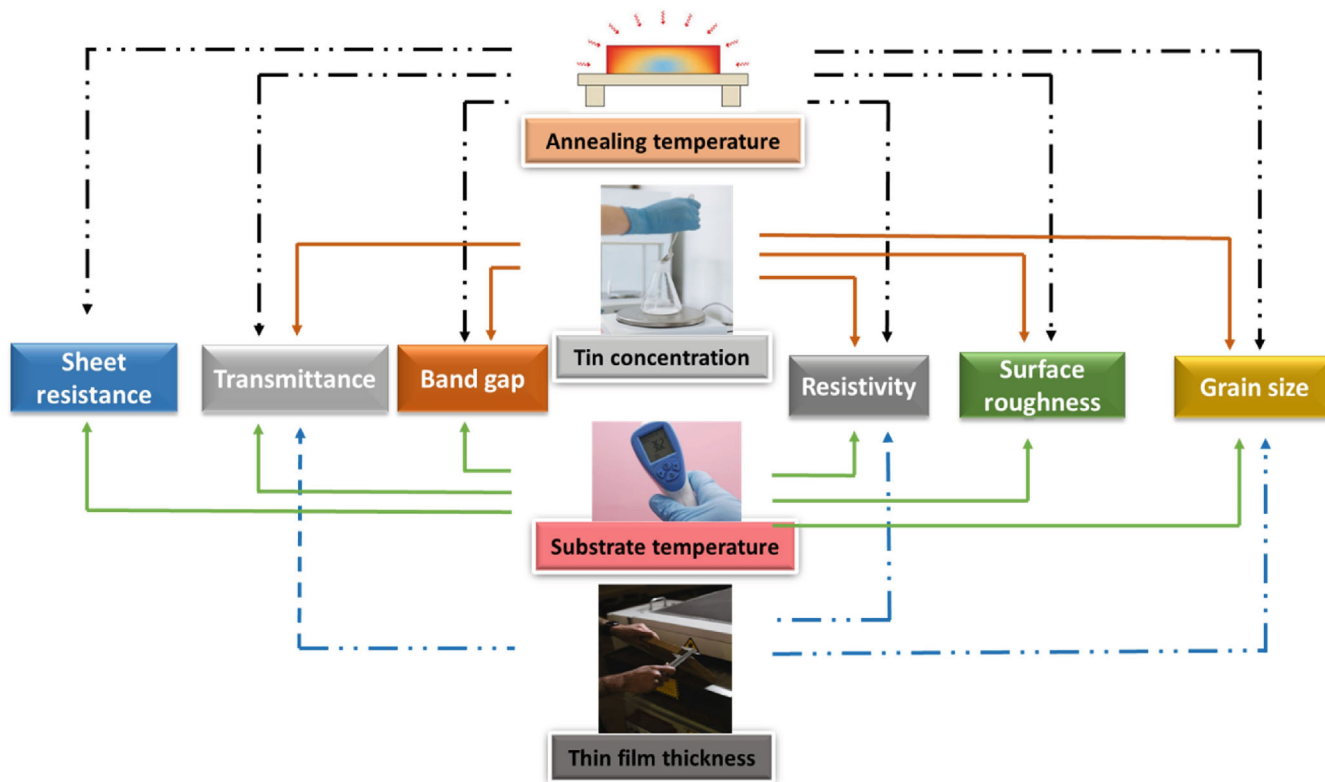


FIGURE 3 ITO thin films parameters relationship with annealing and substrate temperature, tin concentration, and ITO thin films thickness (authors original images, attribution is not required to part of the figures).

of annealing, the carrier concentration increased four times, and carrier mobility doubled compared to the as deposited thin film shown in Figure 4A. After the annealing process, the carrier concentration was $4.8 \times 10^{20} \text{ cm}^{-3}$ carrier mobility was $20 \text{ cm}^2 \text{ V}^{-1} \text{ s}^{-1}$. The thin film resistivity was minimized to 5.7×10^{-4} from $6 \times 10^{-3} \Omega \text{ cm}$ with improved the crystallization. Alam and Cameron¹ investigated the relationship of between annealing temperature and resistivity ITO (10 wt.% Sn-doped) thin film prepared by the sol-gel dipping process. In this example, the annealing temperature ranged from 300°C to 700°C, with samples being annealed at each temperature (300°C, 400°C, 500°C, 600°C, and 700°C) for 1 h. Analysis showed that the electrical resistivity of ITO thin film decreased with increasing annealing temperature from 300°C to 600°C (Figure 4B). The resistivity of thin film calculated by using $\rho = \frac{(2\pi m^*kT)^{\frac{1}{2}}}{De^2 n \cdot \exp\left(\frac{-eV_B}{kT}\right)}$.⁹¹ Additionally, the optical properties of the thin films were enhanced with annealing temperature. The transmittance improved from 80% for 400°C annealing to 93% for 600°C annealing, as shown in Figure 4C. The high-temperature (at 700°C) annealing guided thin films' steeper observation curve and showed excellent crystallinity and small defect density close to the band edge. The XRD spectrum (Philips X'pert XRD system, Cu target) analyses showed polycrystalline with a cubic bixbyite structure with no phases corresponding to the tin. Here, the tin was fully doped with In_2O_3 at an annealing temperature of 600°C. The SEM images of crystalline structure formation showed a clear improvement from as deposited to annealed thin film, as shown in Figure 4D,E. Beaurain et al.⁹² investigated the effect of annealing temperature on the electrical properties of ITO thin films made through spin coating. The findings showed that as the annealing temperature increases, the resistivity of the ITO thin film decreases. The ITO crystalline morphology was found to be different after annealing, with the grain size increasing at 550°C compared to 350°C. Indeed, smaller crystalline sizes result in higher resistivity. However, the high-temperature annealing process is not applicable for ITO deposited on polymer (plastic) or textile substrates. Some applications require low-temperature annealed ITO thin films, especially when polymer or textile substrates need to remain flexible and have certain advantages.

Cho and Yun²⁶ deposited ITO thin film on a glass substrate by a sol-gel spin coating method. The ITO thin film was fired at 450°C–600°C for 30 min in air, followed by cooled down and then annealed at 450°C–600°C for 30 min under Ar (argon) atmosphere. The firing temperature of 500°C made the oxygen vacancies (creating free electrons) in the thin

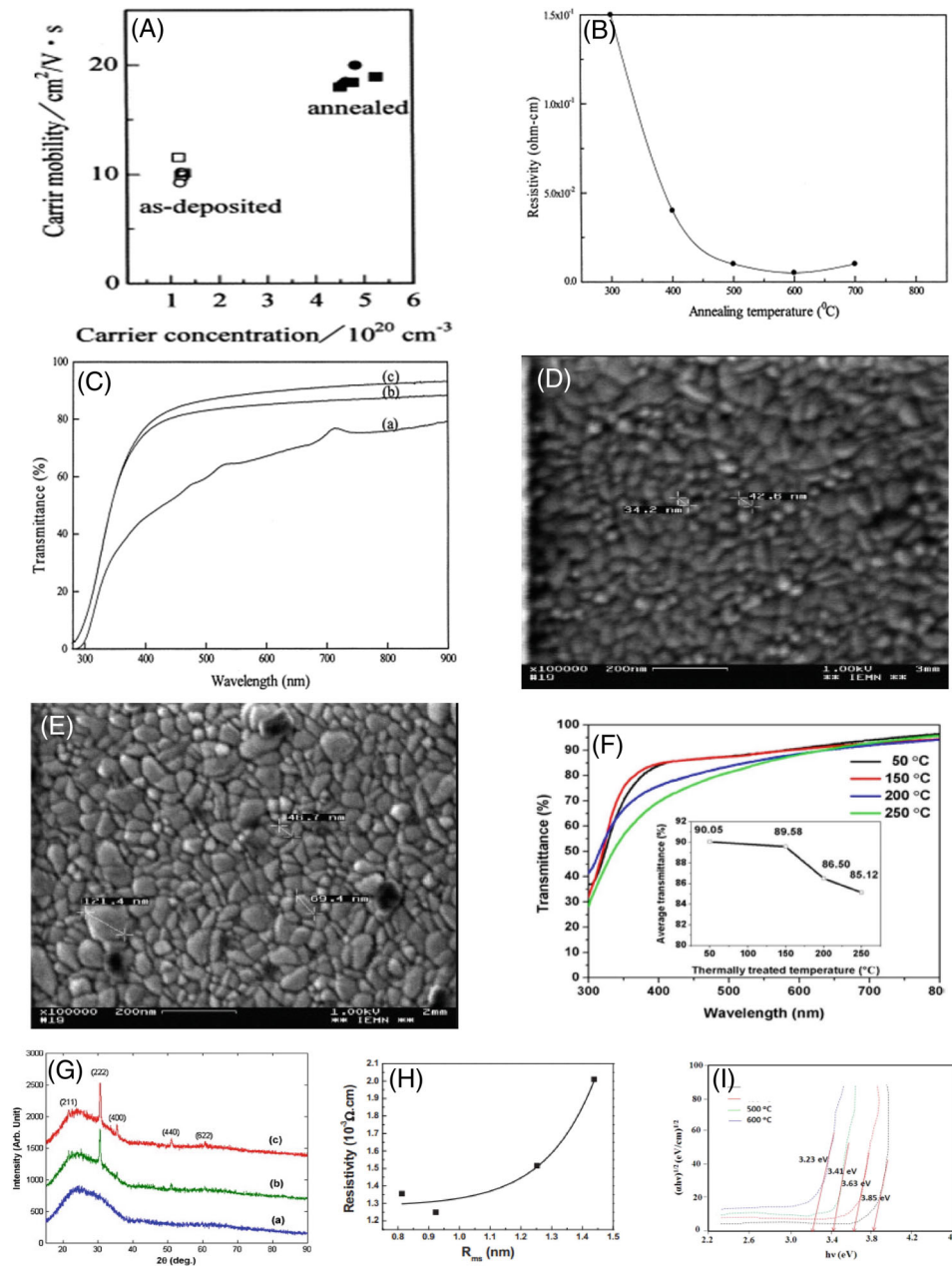


FIGURE 4 (A) Carrier concentration and carrier mobility relationship with annealing temperature (reproduced with permission),³ (B) resistivity changes based on the annealing temperature of the ITO thin film (reproduced with permission),¹ (C) transmittance with the annealing temperature (a) 400°C , (b) 500°C , (c) 600°C (reproduced with permission),¹ (D) SEM image of as-deposited ITO thin film, and (E) annealed at 400°C (reproduced with permission),⁸⁹ (F) transmittance of the ITO thin films thermally treated (reproduced with permission),²⁷ (G) XRD pattern of the ITO thin films (a) as deposited, (b) 200°C , (c) 300°C for 1 h (reproduced with permission),⁴⁰ (H) relationship between surface roughness and resistivity (reproduced with permission),⁹⁰ and (I) electrical bandgap with the annealing temperature reproduced with permission.³¹

film. The thin film subsequently annealed at 500°C showed a lower sheet resistance (resistance of thin sheet multiplied by thickness) close to $361 \Omega/\text{area}$ (author Cho and Yun²⁶ did not specify the area unit) with a grain size of 20–30 nm. However, the resistance increased above 500°C annealing due to microcracks on the thin film, which was observed by the field emissive – scanning electron microscope (FE-SEM, JSM-6430F.JEOL, Japan) at higher (600°C) annealing temperatures.

Dong et al.²⁷ synthesized an ITO thin film on the glass substrate by spin coating method with oxalic acid as a stabilizer and methylcellulose as a binder to enhance the conductivity and transmittance. The thin film was thermally heated

at 50°C, 150°C, 200°C, and 250°C before the final annealing at 500°C for 30 min with a heating rate of 5°C min⁻¹. Subsequently, the thin film was annealed in Ar/H₂ to decrease the resistivity further. The thermal treatment at 50°C removed moisture from the thin film, and temperatures above 150°C resulted in a small amount of crystallization, with a resulting thickness of 115 nm. Thermal treatment at 250°C increased the thickness to 180 nm. The increase in grain size led to a longer electron path, reducing grain boundary scattering. Consequently, this thin film exhibited increased conductivity and low resistivity (4.14 × 10⁻³ Ω cm). Note: analytically, the crystalline sizes can be calculated using the equation ($d = \frac{K\lambda}{\beta \cos\theta}$), where d is the mean crystalline size, K is the shape factor 0.89, λ is the wavelength of the incident beam, β is the full width at half maximum, and θ is the Bragg's angle.

Analysis showed that the resistivity decreased from 8.39 × 10⁻³ Ω cm to 4.14 × 10⁻³ Ω cm, and mobility increased from 7.59 to 14.80 cm² V⁻¹ s⁻¹ concerning the thermal treatment from 50°C to 250°C. A carrier concentration increase led to a bandgap of 3.75 eV at 150°C. The transmittance of ITO thin film decreased with increasing thermal treatment temperature from 50°C to 250°C due to a higher number of pores in the low-temperature level. The pores acted as a channel light that directly passed through ITO thin film. A high-temperature treatment reduces pores and resists light transmission, as shown in Figure 4F.

Daza et al.¹⁶ deposited ITO (10 wt.% Sn, 99.99% purity from Cathay Advance Materials Limited, China) thin film on the glass substrate through RF sputtering at different inclination angle ($\alpha = 0^\circ, 40^\circ, 60^\circ$ and 80°) and annealed samples at 250°C in air for 1 h. The high temperature was avoided to prevent the crystallization of negative optical effects on red and infrared regions. The XRD pattern showed that the most intense peak of In₂O₃ (222) was with other small peaks of $\alpha = 0^\circ$ and 40° after annealing. Angle 80° samples showed surface defects due to the increased strain (ϵ) effect. The bandgap increased to 3.927 eV from 3.748 eV after annealing, and it was calculated using the equation $Eg = h\frac{c}{\lambda}$.⁹³ The porosity of thin film increased with the inclination angle due to fissures in the films, and the refractive index decreased from 2.1 to 1.95.

Raoufi et al.⁴¹ deposited ITO (10 wt.% Sn, 99.99% purity from Merck Co) thin film on a glass substrate (temperature of 25°C) by EB evaporation (base pressure of 1 × 10⁻⁶ mbar, voltage of 1–10 kV, EB current 10–12 mA). Samples were annealed in a thermal furnace in the air at 200°C and 300°C for 1 h. The XRD patterns of the as deposited and different annealing temperatures are shown in Figure 4G. No diffraction peaks appeared on the deposited (non-annealed) ITO thin film. The peaks started to appear with the annealing temperature increased. The diffraction peaks (222) on the annealed (300°C) thin films confirmed the formation of the polycrystalline ITO thin films. The annealing process aggregated native grains into larger clusters. The larger clusters influenced and increased the surface height roughness (R_a) and the standard deviation of surface height values (RMS or root mean square roughness) as the annealing temperature increases. Raoufi et al.⁴⁰ also demonstrated and confirmed the surface roughness and RMS linear behavior with respect to the annealing temperature. The resistivity was found to be directly related to the surface roughness (Figure 4H). As a result of that, resistivity increased with higher annealing temperature due to the increased surface roughness. High surface roughness increased the possibility of electron scattering and reduced the accessible path for electrons.⁹⁰ Measuring the surface roughness of ITO thin films through SEM techniques is challenging. The UV–vis–NIR spectra technique and a calculation were developed to determine surface roughness using the formula $\delta = \frac{\lambda}{4\pi \cos\theta_0} (TIS)^{\frac{1}{2}}$, where δ – surface roughness, λ – wavelength, θ_0 – angle of incidence, TIS – total integrated scattering.⁹⁴

Premkumar and Vadivel³¹ deposited ITO (20 wt.% Sn, Sigma Alrich) thin film on the glass substrate by a spin coating method and annealed it at 300°C, 400°C, 500°C, and 600°C for 2 h. SnO₂ peaks were detected at lower annealing temperatures, and Sn was not fully doped with the In₂O₃. However, at higher annealing (600°C) temperature, no SnO₂ peaks were evidenced, which confirmed that Sn atoms were fully doped on the In₂O₃. The peak intensity also increased, confirming that the annealing process improved thin films' crystallinity. The average crystalline size increased with annealing temperatures of 300°C, 400°C, 500°C, and 600°C (25, 34, 46, and 55 nm), respectively. The electrical bandgap decreased from 3.85 to 3.23 eV with the change in annealing temperature, as shown in Figure 4I. However, a high temperature (above 600°C) of the annealing process led to microcracks and negatively affected the thin film performance. Previous studies on annealing temperatures below 700°C have also reported similar findings. Recently, Li et al.⁴⁵ conducted a study on the conductive stability of ITO thin film fabricated by the PLD deposition technique using high-temperature annealing (above 1000°C). The study observed minimal level of changes in conductivity, grain size, and crystallization of ITO thin film. Furthermore, increasing the annealing process time at 1000°C did not significantly influence the thin film properties. Based on these results, the low temperature is more appropriate to enhance the properties compared to 1000°C. The resistivity and surface roughness decrease, the polycrystalline structure forms, bandgap decreases, and transmittance increases through annealing at lower temperatures.

3.2 | Substrate temperature

The substrate temperature during deposition is one of the essential factors influencing ITO thin film performance. El Akkad et al.¹⁷ and Nisha et al.²² suggested that the thin film's electrical conductivity depends on substrate temperature and film composition. The substrate temperature plays a significant role in the crystallization of thin film during deposition and enhances their properties. The polycrystalline structure formation reduces the thin film resistivity and surface roughness. The high substrate temperature deposition process is like the annealing process. However, it is important to note that it is challenging to maintain the constant substrate temperature.

George and Menon³⁹ deposited ITO (20 wt.% Sn, 99.99% purity) thin film on a glass substrate by EB evaporation (vacuum pressure of 5×10^{-5} mbar, voltage of 6 kV, EB current 10–15 mA) and investigated the relationship between substrate temperature (50°C–350°C) and ITO thin film properties. Analysis indicates that the thin film's resistivity decreased with increasing substrate temperature due to the improved crystalline nature of thin film, shown in Figure 5A. Also, the lowest resistivity of 3×10^{-6} Ωm and a bandgap of 3.41 eV were obtained at a substrate temperature of 350°C.

Craciun et al.⁴⁴ utilized the UV-PLD method to develop the ITO thin films. They aimed to develop high-quality ITO (10 wt.% Sn, 99.99% purity) thin films using the UV-PLD approach with substrate temperatures range from 40°C to 180°C. The thin film showed an amorphous structure up to a substrate temperature of 70°C with high carrier mobility. Good crystallization was observed at 120°C along with the increased resistivity and decreased carrier mobility due to the crystallization, resulting in the formation of a defective layer in the structure.

Marikkannu et al.⁵⁸ deposited ITO (5 wt.% Sn) thin film on the glass substrate by jet nebulizer spray (JNS) pyrolysis with three different substrate temperatures (350°C, 400°C, and 450°C). As a result of substrate temperatures, a fine crystallization development confirmed by the presence of peaks (222) (400) in the XRD patterns, shown in Figure 5B. The SEM images of different substrate temperatures (Figure 5C–F), also confirmed effect of substrate temperature on crystallization. As a part of this study, transmittance was investigated in relations with the substrate temperatures. Thin film transmittance was studied by UV – Vis optical transmittance spectra from 300 to 800 nm wavelength range. The transmittance (60%, 70%, and 80%) of thin film increased with ITO thin film prepared temperature increases (350°C, 400°C, and 450°C) due to crystallization (Figure 5G). Sun, Wang and Kwok²³ deposited single ITO (10 wt.% Sn, 99.99% purity) thin film on a glass substrate and applied ZnO buffer before the ITO thin film using RF sputtering (Denton Vacuum, Inc model DVI SJ/24LL) with substrate temperatures ranging from 50°C to 300°C. Figure 5H illustrates the resistivity relationship with substrate temperature. Both the ITO-coated samples' resistivity decreased with increasing temperature. Especially the ZnO buffer on the glass substrate with ITO showed significantly lower resistivity due to an increase in oxygen ratio, reducing the electron scattering with concerning same substrate temperature conditions.

Jun et al.²¹ initiated an investigation to determine deposition conditions for achieving high transmittance and low sheet resistance. ITO (10 wt.% Sn) thin film (300 nm) was deposited on a glass substrate using RF sputtering (AJA ATC2000) with a substrate temperature range from 30°C to 230°C. The analysis showed that the layer resistance decreased significantly with higher temperature deposition. The lower sheet resistance of $28 \Omega \text{m}^{-1}$ was obtained at a substrate temperature of 230°C, RF power of 125 W, and pressure of 2 Pa. However, the transmittance decreased compared to other RF power (75 and 100 W) deposition conditions due to the low oxygen vacancies concentration formed. Increasing temperature enhanced the transformation of the ITO thin film from an amorphous to a crystalline structure. Higher RF sputtering power levels also enhanced the crystallinity of ITO thin film, as high power quickly melted particles and resulted in a higher degree of crystallinity.

Nisha et al.²² deposited ITO (5 wt.% Sn, 99.99% purity) thin film on a glass substrate by RF sputtering with a substrate temperature range from room temperature to 250°C. Initially, XRD analysis confirmed the linear relationship between the intensity of peaks (222) (400) and substrate temperature, indicating crystallization. As a result, the resistivity of ITO thin film decreased with an increasing substrate temperature, shown in Figure 5I. The resistivity and sheet resistance decreased with an increase in substrate temperature up to 150°C. The grain size increased with deposition temperature, reducing grain boundary scattering, and increasing conductivity. Carrier mobility and carrier density increased with substrate temperature, leading to a significant decrease in resistivity, particularly around 150°C, due to better crystallinity of the thin film. The increase in resistivity observed above 200°C might be attributed to alkali ions contamination on the glass substrate. Transmission also increased with substrate temperature. High substrate temperature (250°C) showed an average above 80% transmission in the visual spectrum region. Substrate temperature also influenced the bandgap, which increased from 3.68 to 3.84 eV with the increase in substrate temperature.

Hoshi et al.⁶² investigated the sputtering deposition rate relationship with the ITO thin film on glass, acryl, and polycarbonate substrate in low substrate temperature (below 80°C). As results showed that on a polycarbonate film substrate,

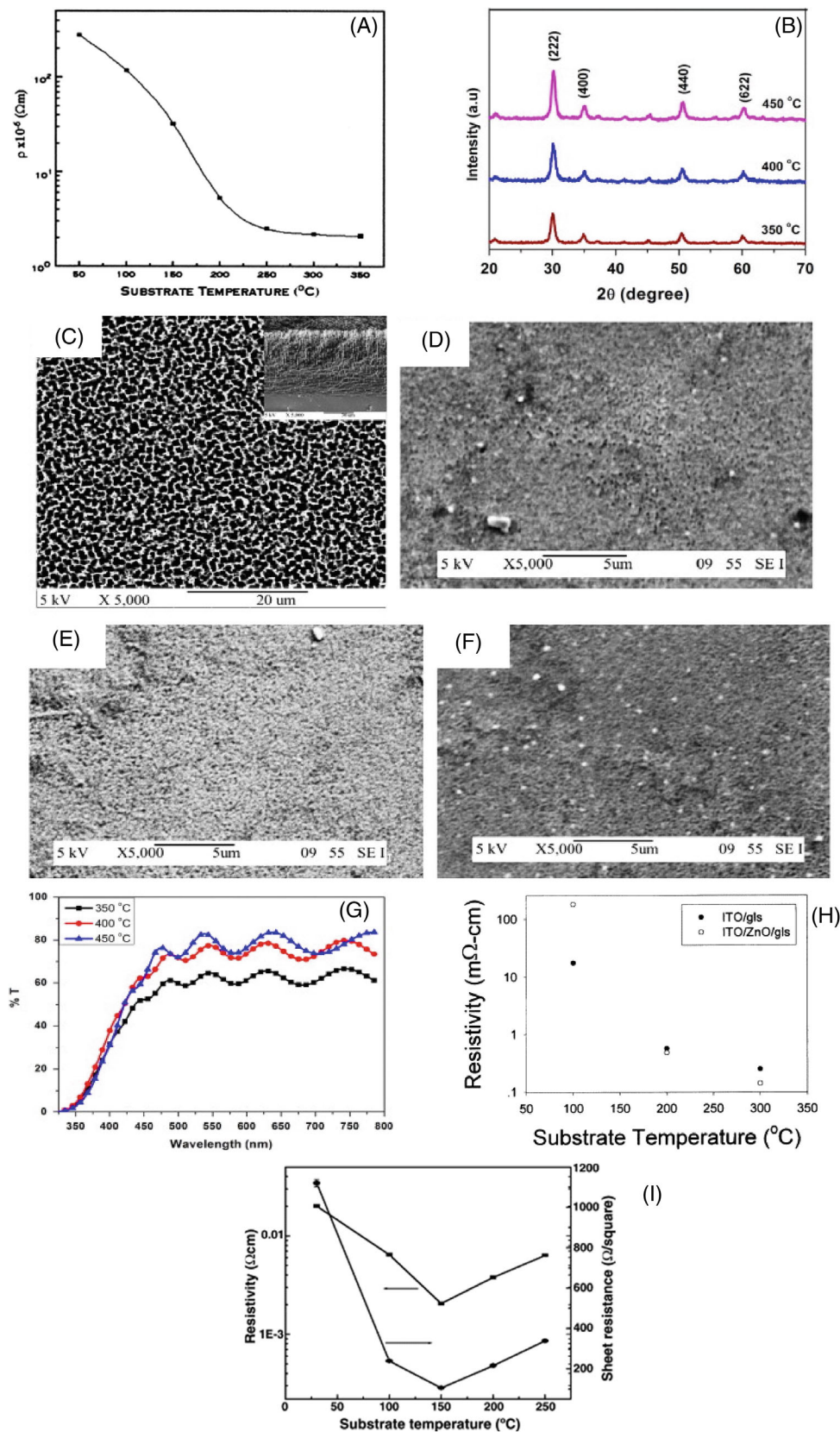


FIGURE 5 (A) Resistivity vs substrate temperature (reproduced with permission),³⁹ (B) XRD patterns based on substrate temperature (reproduced with permission),⁵⁸ SEM images of ITO thin films: (C) silicon substrate, ITO deposited temperatures (D) 350 $^{\circ}\text{C}$, (E) 400 $^{\circ}\text{C}$, and (F) 450 $^{\circ}\text{C}$ (reproduced with permission),⁵⁸ (G) transmittance of the ITO thin films with different temperature preparation (reproduced with permission),⁵⁸ (H) films resistivity based on the substrate temperature (reproduced with permission),²³ and (I) resistivity and sheet resistance based on the substrate temperature (reproduced with permission).²²

the deposition rates up to 53 nm min^{-1} thin films showed the lowest resistivity, with crystal growth promoted by a high oxygen flow rate. However, above 80 nm min^{-1} deposition rate, the thermal damage obtained on polycarbonate substrate by sputtering film fabrication. A facing target sputtering (FTS) system effectively deposit a rate of 100 nm min^{-1} thin films on an acryl substrate without thermal damage. Maintaining an appropriate substrate temperature helps in crystallization, grain size increase, and resistivity decrease of thin film. However, it is not always viable to maintain the substrate temperature in all types of thin film preparation, and in some cases, it requires special arrangements to keep the substrate temperature constant.

3.3 | Tin concentration

The concentrations of tin and indium chloride are critical factors in deciding ITO thin film properties. The optical properties and crystallization of ITO thin films strongly depend on Sn-doped concentrations.⁴² The XRD results confirmed that doped films have larger crystalline size than non-doped ones. The electrical resistivity also depends on the atomic ratio of Sn:In of thin film.⁴⁷

Nishio, Sei and Tsuchiya⁸⁰ investigated the thin film's electrical resistivity, carrier concentration, and mobility in the SnO_2 range of 0 mol% to 20 mol% with heat treatment from 400°C to 800°C for 1 h. In this study, a dip coating process was used to deposit the ITO thin film on a glass substrate. They observed low electrical resistivity of $1.2 \times 10^{-3} \Omega \text{ cm}$, a high carrier concentration of $1.2 \times 10^{20} \text{ cm}^{-3}$, and more downward carrier mobility of $7.0 \text{ cm}^2 \text{ V}^{-1} \text{ s}^{-1}$ at 12 mol% SnO_2 . It was concluded that the crystallization of thin film started at a heat treatment temperature of 400°C and was fully crystallized at 600°C .

George and Menon³⁹ deposited ITO thin film on a glass substrate by EB evaporation with a tin oxide concentration level ranging from 10% to 80%. As a result, the thin film's resistivity increased with the percentage of tin oxide above 20%. The 20% and 40% concentrations of tin oxide resistivity increased from $7.6 \times 10^{-3} \Omega \text{ m}$ to $1.92 \times 10^{-2} \Omega \text{ m}$. Further increase of tin oxide has not shown high resistivity changes, as shown in Figure 6A. Analysis showed that the initial resistivity decreased due to the gradual incorporation of tin impurity as dopants into the indium lattice. Also, the resistivity increased above 20% of tin concentration because of a higher concentration of tin than the optimum level, resulting in more tin ions settling in neighboring sites, which led to crystalline disorder.¹

Senthilkumar et al.⁴² deposited ITO thin film on a glass substrate by EB evaporation (base pressure of $5 \times 10^{-5} \text{ mbar}$, voltage of 1–10 kV) with tin concentrations ranging from 0% to 20% to determine the optimal parameters for ITO thin film fabrication. The analysis suggests that the thin film grain size decreased with increasing tin concentration, as shown in Figure 6B. XRD results confirmed that ITO thin film had a partially crystalline nature, which was the reason for weak intensity peaks with tin concentration increases. Transmittance showed high level at zero tin (Sn) concentration, and the increase in Sn concentration led to an increase in oxygen vacancies and more grain boundaries. The scattering of light at the grain boundaries was the cause of the decrease in transmittance. An energy bandgap was 3.61 eV at zero tin concentration and increased to 3.89 eV at 20 wt.% of tin concentration. Alam and Cameron¹ deposited ITO thin film on a glass substrate by sol-gel dipping process with a tin concentration ranging from 0% to 20% and investigated the wt.% of Sn with electrical resistivity at an annealing temperature of 600°C . They concluded with 10 wt.% Sn-doped thin film showed lower resistivity of $1.5 \times 10^{-3} \Omega \text{ cm}$ (Figure 6C).

The concentration level of indium chloride (InCl_3) can also influence ITO thin film properties. Moholkar et al.⁵⁹ investigated the effect of indium chloride concentration on the properties of ITO thin film by preparing ITO thin film using the spray pyrolysis method with increasing concentration level of InCl_3 from 6.25 to 37.5 mM. The thin film thickness increased with an increasing concentration level of InCl_3 from 6.25 to 25.0 mM spraying solution, shown in Figure 6D. However, the thickness decreased after 25 mM due to available oxygen from the H_2O molecule was insufficient to increase growth further. Additionally, the resistance decreased with increasing InCl_3 concentration. The low electrical resistance of thin film achieved in the deposition of InCl_3 25 mM concentration at substrate temperature 500°C is shown in Figure 6E. Atomic force microscopy (AFM) imaging confirmed surface roughness and grain sizes were 35 nm and around 200 nm, respectively, for a thin film with InCl_3 25 mM concentration. The tin concentration on the thin film is important to decide the performance of the thin film without annealing and other processes. An appropriate tin concentration results in the low resistivity, grain size, and Sn-doped crystalline structure. However, a high concentration of tin increases the resistivity of a thin film. Therefore, the right amount of tin concentration enhances the performance of the thin film, which can be further improved through the annealing process.

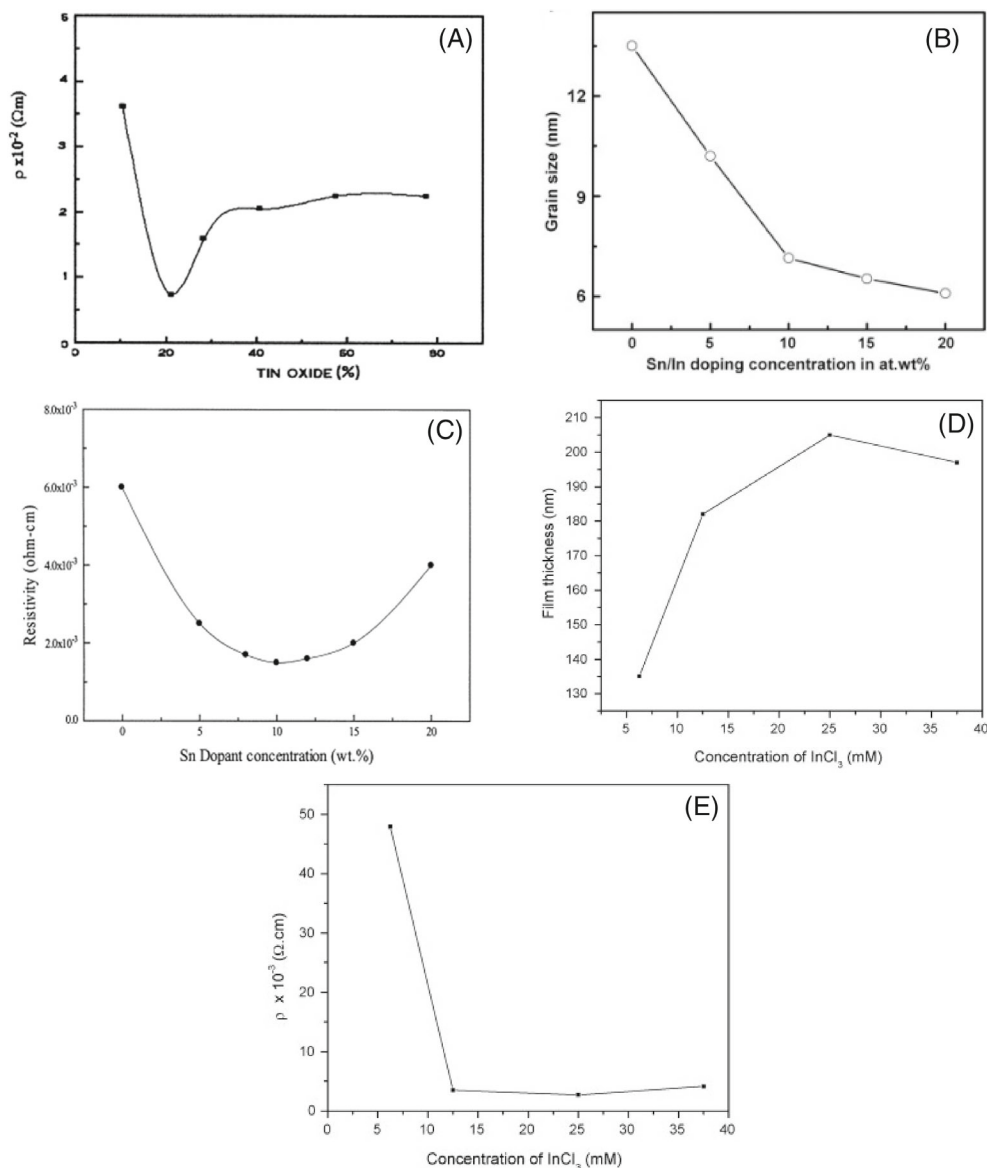


FIGURE 6 (A) Resistivity vs tin oxide concentration (reproduced with permission),³⁹ (B) variation of the grain size with respect to the Sn-doped concentrations (reproduced with permission),⁴² (C) resistivity changes based on the Sn wt % at annealing temperature of 600°C (reproduced with permission),¹ (D) The thin film thickness increases with the concentration of the InCl_3 (reproduced with permission),⁵⁹ and (E) the electrical resistivity changes based on the concentration of InCl_3 (reproduced with permission).⁵⁹

3.4 | Thin film thickness

The thickness of ITO thin film is crucial for thin film's resistivity, crystallization, and overall performance. The thickness significantly impacts structural, optical, and electrical properties. A high thickness of the thin film can be made through a high deposition rate in a short time. However, a high deposition rate can affect thin film behavior and the base substrate. Alternatively, a high thickness can be obtained through repeated coating and a low deposition rate for a long time. Gradual growth of thickness reduced resistance significantly. Other parameters of ITO thin film grain size, roughness, relative density, and pore size were influenced by thickness increases.¹¹

Kumar, Raju and Subrahmanyam¹¹ deposited ITO (10 wt.% Sn, 99.99% purity) thin film on a glass substrate by DC magnetron sputtering (ANELVA model SPC-530H) at room temperature. Structural, optical, and electrical properties relationship with thin film thickness was examined through increasing the thickness from 165 to 1175 nm. The crystalline structure was studied using XRD (Philips X'pert Pro), and the diffraction peaks were observed for thicknesses above

380 nm (Figure 7A). The high thickness (1175 nm) showed various peaks indicating crystalline formation. The grain size and RMS roughness increased with thickness, which was attributed to decreases in transmittance. A low resistivity of $4.5 \times 10^{-4} \Omega \text{ cm}$, a high carrier concentration of $5.3 \times 10^{20} \text{ cm}^{-3}$, and low mobility of $26 \text{ cm}^2 \text{ V}^{-1}$ appeared on 545 nm thin film thickness.

Ghorannevis, Akbarnejad and Ghorannevis¹⁸ studied the physical properties of ITO thin films depending on the deposition time during radio frequency magnetron sputtering. The deposition time increased the thin film thickness from 100 to 300 nm and decreased crystal size from 25.440 to 17.226 nm. The SEM and cross-section thickness measurement images are shown in Figure 7B,C. The XRD pattern suggested that diffraction peaks increased with thickness increase, indicating crystalline formation. Surface morphology investigation by AFM showed that grain size and surface roughness (3.93–5.18 nm) increased with thickness from 100 to 300 nm. Increasing the film thickness from 100 to 300 nm reduced resistivity from $15.4 \times 10^{-4} \Omega \text{ cm}$ to $2.43 \times 10^{-5} \Omega \text{ cm}$. At a thickness of 300 nm, lower resistivity was observed due to improved crystallinity, high oxygen vacancy concentration, and grain growth, which decreased grain boundary electron scattering. Increasing thin film thickness reduces resistivity and transmittance while increasing grain size and carrier concentration. However, high thin film thickness increases the resistivity and negatively influences properties. An appropriate thin film thickness enhances the thin film performance, and annealing can further improve the thin film.

Davood and Faegh³⁸ investigated the surface morphology changes on ITO (10 wt.% Sn) thin film on the glass substrate with different thicknesses (ranging from 100 to 350 nm) using the EB deposition method. The AFM study revealed the formation of a porous granular surface as a surface roughness linearly decreased from 24.8 to 4.13 nm, with thickness increasing from 100 to 350 nm. Teixeira et al.¹⁴ studied ITO (10 wt.% Sn, 99.99% purity) thin film deposited on a glass substrate by DC magnetron sputtering with film thickness ranging from 50 to 900 nm. The resistivity of ITO thin film increased with increasing thickness, as shown in Figure 7D. The lower resistivity of $2.35 \times 10^{-4} \Omega \text{ cm}$ was observed with a film thickness of 282 nm due to the physical properties of a thin film being different from bulk material. Initially, the thickness increase reduced the resistivity up to a thickness of 282 nm. However, after that point, the resistivity increased due to the addition of multiple layers, which resulted in decreased carrier mobility and carrier concentration. The formation of voids increased charges carrier scattering, leading to an increase in resistivity with thickness.

Ota et al.³ deposited ITO thin film on a glass substrate by dip coating process with repeated dipping of ten layers, followed by a heating process to dry each layer. A film thickness of 112 nm showed low resistivity of $5.8 \times 10^{-4} \Omega \text{ cm}$ after 10-layer deposition. As previous studies suggested, a thin film thickness of approximately 300 nm showed minimum resistivity of $4 \times 10^{-4} \Omega \text{ cm}$. The resistivity of the thin film is influenced by the deposition method. Herrero and Guillen²⁰ deposited ITO thin film on a glass substrate by RF sputtering with a thickness range of 175 to 450 nm. The XRD results indicated an increase in the intensity of the (222) peak with increasing thickness, indicating crystallization (Figure 7E). The resistivity decreased from $4.0 \times 10^{-3} \Omega \text{ cm}$ to $1.2 \times 10^{-2} \Omega \text{ cm}$, and the transmittance increased from 87% to 91%, with thickness increasing from 175 to 450 nm, due to an improvement in grain size enhancing conductivity (Figure 7F).

Suitable annealing and substrate temperature, tin concentration, and thickness of the thin film makes ITO thin film with properties such as low resistivity, a polycrystalline structure, increased grain size, changes in band gaps and Sn fully doped in In_2O_3 and high transmittance. The low resistance of the ITO thin film obtained at the high grain size condition prevents electron scattering, enhancing the thin film's performance. Morphology changes from amorphous to polycrystalline structure provide a smooth surface roughness. Minimization of the bandgap improves the conductivity of the thin film.

Not all the literatures investigated the ITO thin film crystalline formation with Raman spectroscopy and X-ray photoelectron spectroscopy (XPS). However, few of them are summarized here. Lian et al.⁹⁶ deposited ITO thin film on a glass substrate using EB evaporation. The deposited substrates underwent annealing in the range of 150°C – 450°C . Oxygen vacancy defect states and vibrational levels within the ITO thin films were investigated by Raman scattering. The results confirmed that oxygen vacancy defects have a significant influence on the vibration of the In–O–In bond, causing variations in Raman scattering intensity and shifts in Raman peak position. The reduction in Raman scattering intensity results from a decrease in oxygen vacancies after annealing. The results indicated that partial oxygen vacancies were filled during the annealing process, and the broken In–O–In bonds confirmed the dissociated and agglomerated rough surface. Hong et al.⁹⁷ deposited ITO thin film on a glass substrate using EB evaporation. Subsequently, laser irradiation treatments were performed on the surface of the ITO thin film. Raman spectroscopy was used to study the effects of laser intensities on defect formation and vibrational modes of the ITO thin film. Following laser irradiation, the Raman scattering

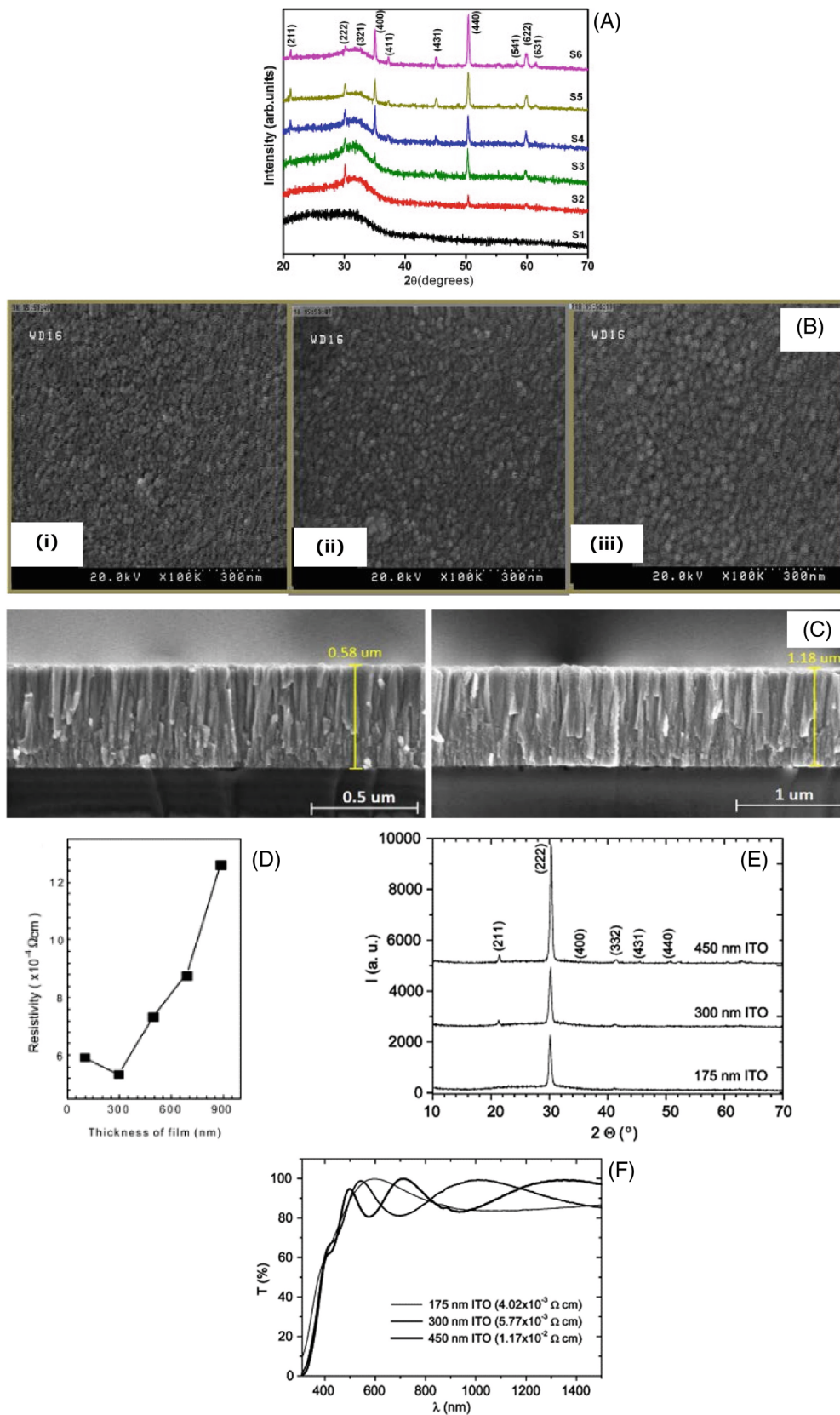


FIGURE 7 (A) X ray diffraction with different thickness (reproduced with permission),¹¹ (B) SEM image of ITO thin films based on thickness (i) 100 nm, (ii) 200 nm, (iii) 300 nm (reproduced under the terms of the Creative Commons Attribution 4.0),¹⁸ (C) cross-section view of thickness (reproduced under the terms of the Creative Commons Attribution 3.0),⁹⁵ (D) the resistivity increases with the thickness (reproduced with permission),¹⁴ (E) XRD results of different thickness films (reproduced with permission),²⁰ and (F) transmittance based on thin film thickness (reproduced with permission).²⁰

intensities of all the irradiated ITO thin films were higher than those of the as-deposited thin films. The high scattering intensity resulted from the laser, which disrupted and recombined the internal structure of the ITO thin film, enhancing intergrain scattering. Additionally, the peak intensities decreased with further increases in laser power, leading to more defects. Another factor to consider is that the removal of ITO thin film thickness resulted in a new vibrational mode at 173 cm^{-1} .⁹⁷ Singh et al.⁸³ deposited ITO thin film on glass substrate by spin coating. Raman analysis of ITO thin film carried out to analyze the vibrational and transmittance features. The Raman results showed and confirmed that SnO_2 along with indium doped SnO_2 observed peaks at 472, 634 and 761 cm^{-1} .

Al-Kuhaili⁹⁸ deposited ITO thin film on the fused silica and molybdenum substrates using DC magnetron sputtering at the substrate temperature of 350°C . XPS used on the molybdenum substrate to see the charging of the non-conducting samples could be reduced. The result peaks showed that, 3d core level of indium and tin consists of two sublevels of $3d_{3/2}$ and $3d_{5/2}$ due to spin orbit splitting. The first component binding energy (BE) of $529.8 \pm 0.2\text{ eV}$, indicates the In-O bond in In_2O_3 and the second component BE of $530.4 \pm 0.2\text{ eV}$, was assigned to the Sn-O bond in SnO_2 . The third component BE of $531.2 \pm 0.1\text{ eV}$ corresponds to the oxygen vacancies in O 1s spectrum. The XPS confirmed that the indium and tin atoms bonded to oxygen and presence of oxygen vacancies in thin films. Hsu et al.⁹⁹ prepared ITO thin films on a glass substrate using plasma-enhanced atomic layer deposition (PEALD). XPS results confirmed the successful incorporation of Sn into In_2O_3 . Two distinct peaks were observed at 486.1 eV and 495.3 eV which corresponded to $\text{Sn}3d_{5/2}$ and $\text{Sn}3d_{3/2}$, respectively. The presence of Sn^{4+} and Sn^{2+} components led to further splitting of each peak. No metallic Sn component peak was observed. However, Sn^{2+} and neighboring Sn^{4+} did not contribute to the improvement of the conductivity of ITO thin films. To achieve higher conductivity and an improved free electron concentration, the peak ratio of Sn^{4+} needs to be increased as much as possible.

Overall, the annealing temperature has a very close relationship with the properties of ITO thin film, as it enhances crystallinity and increases grain size. As per various studies presented above, an annealing temperature of typically 600°C is very appropriate for excellent properties of the ITO thin film. It is important to note that high annealing temperature can induce microcracks (potentially due to thermal strain) in the thin film and increase resistance. The substrate temperature is also significant in deciding the performance of thin film. The crystallinity of thin film improves with an increase in substrate temperature. However, a substrate temperature (above the room temperature) is not applicable for all the preparation conditions due to the need for special arrangements to keep the temperature stable. A substrate temperature maximum of 200°C is suitable for getting a better property of thin film. Higher substrate temperature can result in alkali contamination on a glass substrate, increasing resistivity. The optical properties of ITO thin film are highly dependent on the tin concentration. Higher tin concentration level shows better doping of In and Sn. A maximum tin of 10% is suitable for thin film. Higher tin concentration can lead to crystalline disorder. Thickness has a very significant impact on thin film properties. Increasing thickness improves the grain size and crystallinity. The ideal thin film thickness is approximately 300 nm for ITO thin film preparation. Higher thickness deposition increases the resistance of the thin film.

4 | ITO THIN FILM FOR HUMIDITY SENSING APPLICATIONS

This section discusses the ITO thin film humidity sensing and its limitation.

In a recent review on humidity sensor, Kuzubasoglu¹⁰⁰ discussed that the conductivity of a thin film is influenced by the sensitive signals from water molecules in humidity-sensitive materials. Hydrogen bonds, intramolecular contacts, electrostatic interactions, hydrophilic and hydrophobic interactions, chemical bonding, etc, are some types of these interactions. To describe the process of humidity sensing, two different humidity sensing mechanisms (chemisorption, physisorption) are significant. The chemisorption mechanism is significant at low relative humidity (or high temperatures), and physisorption plays a key role at high relative humidity (or low temperatures). Chemisorption relates to the chemical reaction between water molecules and the material, while physisorption is more related to the physical absorption of water molecules by van der Waals forces. In a wide range of applications, combined sensing mechanisms take place.¹⁰¹ Figure 8A,B illustrates the mechanism of ITO thin film interaction with H_2O molecules in the context of humidity sensing. The sensitivity of ITO to humidity primarily depends on the low and high reactive humidity levels. In lower relative humidity level (about 10%–30%), H_2O (water) molecules absorb on ITO thin film and applied DC voltage ionizes water molecules into H^+ and OH^- . In the higher relative humidity range (about 50%–90%), a multilayer of H_2O molecules produces hydronium groups (H_3O^+) and OH^- as electron donors, leading to a decrease in thin film resistance and an increase in electronic mobility and conductivity as well.³¹ The schematic diagram (Figure 8C) presents the measurement of resistance/capacitance changes based on the humidity level on the ITO thin film.

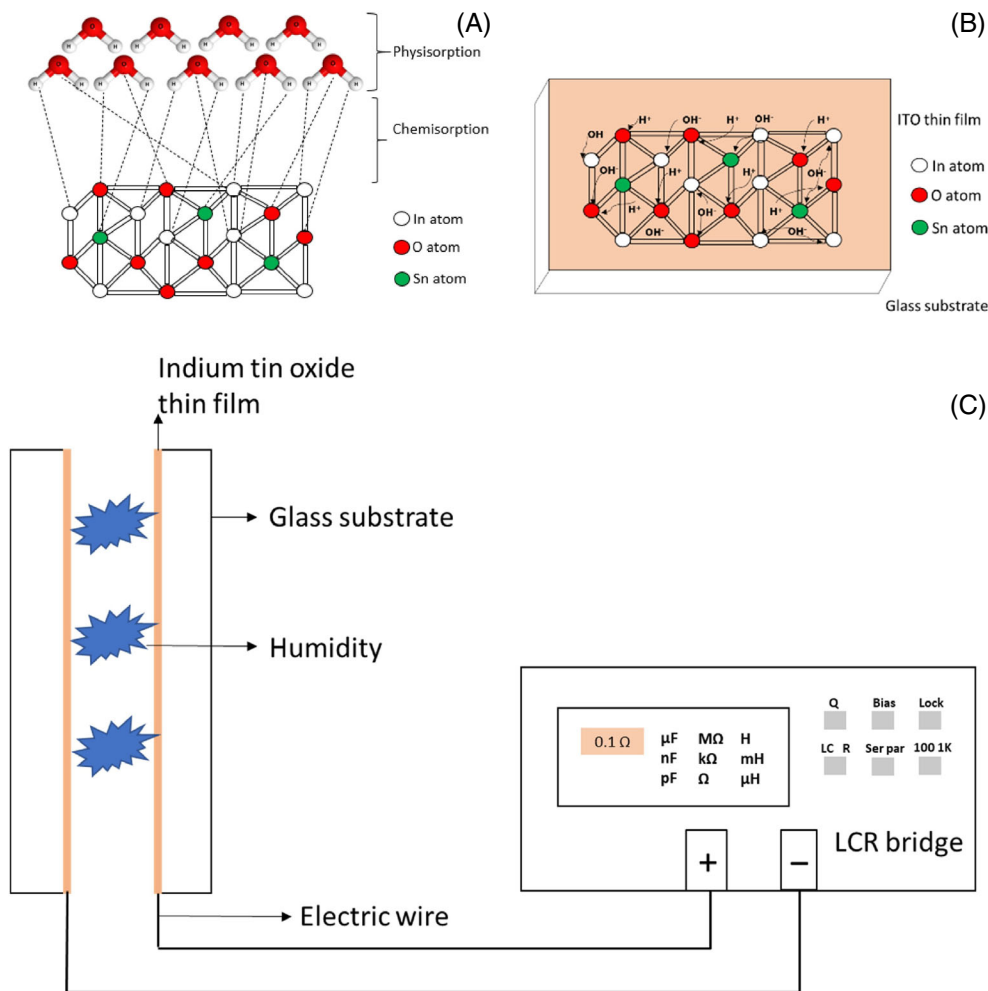


FIGURE 8 (A) Water molecules absorption on ITO, (B) water molecules interaction with ITO thin film, and (C) capacitance/resistance measurement based on humidity (authors original image).

Table 1 presents different ITO thin film humidity measurement approaches based on resistance and capacitance changes. It has been demonstrated that capacitance increases with the presence of relative humidity range increase. Based on the previous studies, the resistance of ITO thin film decreased with an increase in relative humidity ranging from 5% to 98% at room temperature due to the many electrons released from water molecules. Premkumar and Vadivel³¹ investigated ITO thin film humidity sensing behavior based on thin films annealed at different temperatures. Figure 9A shows that the resistance decreases (for different temperature annealed films) with relative humidity ranging from 10% to 90%. The higher temperature annealed thin film showed larger resistance drops due to improved crystallite and smaller energy bandgap. The sensitivity of ITO thin films is shown in Figure 9B. Analysis showed that the sensitivity range increased with an increase in relative humidity for all thin films. However, the thin film annealed at higher temperatures demonstrated higher sensitivity and greater stability compared to one annealed at low temperatures.

The response and recovery times are significant for evaluating humidity sensing efficiency of thin film. Premkumar and Vadivel³¹ demonstrated humidity thin film performance of low temperature (i.e., 300°C) annealed thin film had an average response and recovery time of 68 and 54 s, respectively. The response and recovery time were enhanced at higher temperature (600°C), annealing 28 and 14 s. High-temperature annealed thin film showed high sensitivity, quick response, and recovery time compared with other annealed thin film shown in Figure 9C. Babu and Vadivel²⁵ showed sensitivity, response, and recovery time behavior of thin film as prepared (without annealing) and after annealing (at 500°C). Resistance decreased from 65 to 15 MΩ with an increased relative humidity of annealed thin film. As prepared, ITO thin film showed that resistance decrease from 90 to 45 MΩ with an increase in relative humidity. The humidity sensitivity performance of annealed ITO thin film increased with an increase in relative humidity. The response and

TABLE 1 Overview of ITO thin film humidity sensing measurement.

Sr. no	Materials	Measuring parameter	Type	Measuring instrument	References	Remarks
1	ITO and aluminum oxide ink (as a layer applied)	Capacitance	Active	UNI-T digital LCR meter (UT612)	McGhee et al., ⁵⁰ ; McGhee et al., ⁵⁴	Traditional parallel two plates
2	ITO applied on glass plate	Resistance	Active	Keithley Electrometer	Yadav et al., ³⁴	–
3	ITO applied on glass substrate	Resistance	Active	LCR bridge	Babu and Vadivel, ²⁵	Traditional parallel two plates
4	ITO thin film on glass substrate	Resistance	Active	LCR bridge	Premkumar and Vadivel, ³¹	Traditional parallel two plates. Annealing temperature enhanced the sensitivity of ITO.

recovery time of annealed thin film enhanced compared to the as-prepared thin film, as shown in Figure 9D. Note, the sensitivity of ITO thin film can be evaluated with LCR bridge (used to measure the inductance (L), capacitance (C), and resistance (R) of components), and the humidity level can be measured through a standard hydrometer³¹: $S = \frac{RH_2 - RH_1}{RH_1} \times 100$, where S is sensitivity, RH_2 is resistivity measured in high relative humidity, and RH_1 is resistivity measured in low relative humidity. RH is also expressed on a scale of hundreds and is called a percentage relative humidity (%RH): $\%RH = \frac{\text{Water vapour pressure}}{\text{Saturated water vapour pressure}} \times 100$.

Yadav et al.³⁴ reported nanocrystalline ITO thin film synthesis and humidity sensing response. ITO thin film was deposited on a glass substrate by the spin coating method with a synthesized $\text{SnO}_2 - \text{In}_2\text{O}_3$ nanocomposite. Followed by that, the thin film was dried at 120°C for 4 h and annealed at 500°C in the furnace to enhance their properties. The surface morphology of nanocomposite thin film studied revealed a nanostructured polycrystalline cluster with space of pores on the thin film's surface. Here, the XRD peaks were accordingly matched with the SnO_2 or In_2O_3 index. The small crystalline size of thin film enhanced thin film's humidity sensitivity. Transmission electron microscope (TEM) image of ITO thin film indicated grain's diameter ranged from 10 to 40 nm, and most grains were spherical. Figure 9G illustrates ITO thin film humidity response in three stages (I, II, and III). A rapid decrease in resistance value was observed in the low humidity range of 10% to 40% (stage I). In mid of humidity level (stage II), from 40% to 70% resistance range of decrease was small compared to stage I. In stage III, high humidity levels from 70% to 90% resistance decrease observed gradually. The sensitivity was found to be 1.20 MΩ/%RH, 0.61 MΩ/%RH, and 0.09 MΩ/%RH of ITO concerning the humidity level in three stages I, II, and III, respectively. The average sensitivity was 0.68 MΩ/%RH. The ITO thin film showed a high humidity sensitivity (1.20 MΩ/%RH) in low relative humidity concentration. In lower relative humidity, water molecules were absorbed on thin film dominant for electrical conduction. In high relative humidity, the amount of water molecules was high and provided a high number of electron donors, which reduced resistance at a high level. The water absorption enhanced electrical conductivity by increasing the electrical charge carriers and protons in the thin films' water absorption system. The electrical conductivity also depended on pores on the thin film surface. The resistance of thin film varied with RH measured using Keithley electrometer.

McGhee et al.⁵⁰ and McGhee et al.⁵⁴ tested the humidity sensing response of thin film designed by a screen-printed process. ITO and aluminum oxides (Al_2O_3) were utilized to make the metal oxide thin films through screen printing with two different surface area (4 and 9 cm²). ITO electrodes were printed and cured in a box oven at 105°C on polyethylene terephthalate (PET) substrates. The alumina dielectric ink was printed on top of dielectric film 1–6 times to eliminate the pinholes. In each print, the alumina dielectric ink was cured at 105°C, and the advantage of alumina was that it provided high capacitance. The final layer of ITO thin film and silver contacts were printed and cured at 105°C for 5 min. The UNI-T Digital LCR meter (UT612) was used to measure the capacitance (recorded in UNI-T digital multimeter software). Based on relative humidity increase (from 5 to 95 %RH), the capacitance value changed from 100 to 600 pF at four different frequencies 100 Hz, 1 kHz, 10 kHz, and 100 kHz. The surface area significantly impacted the thin films' response and recovery time. The smaller surface area (4 cm²) thin film showed a quick response time (21.4 s). The larger surface area (9 cm²) thin film showed response and recovery times of 47.2 and 49.5 s, respectively. The larger thin film surface took a

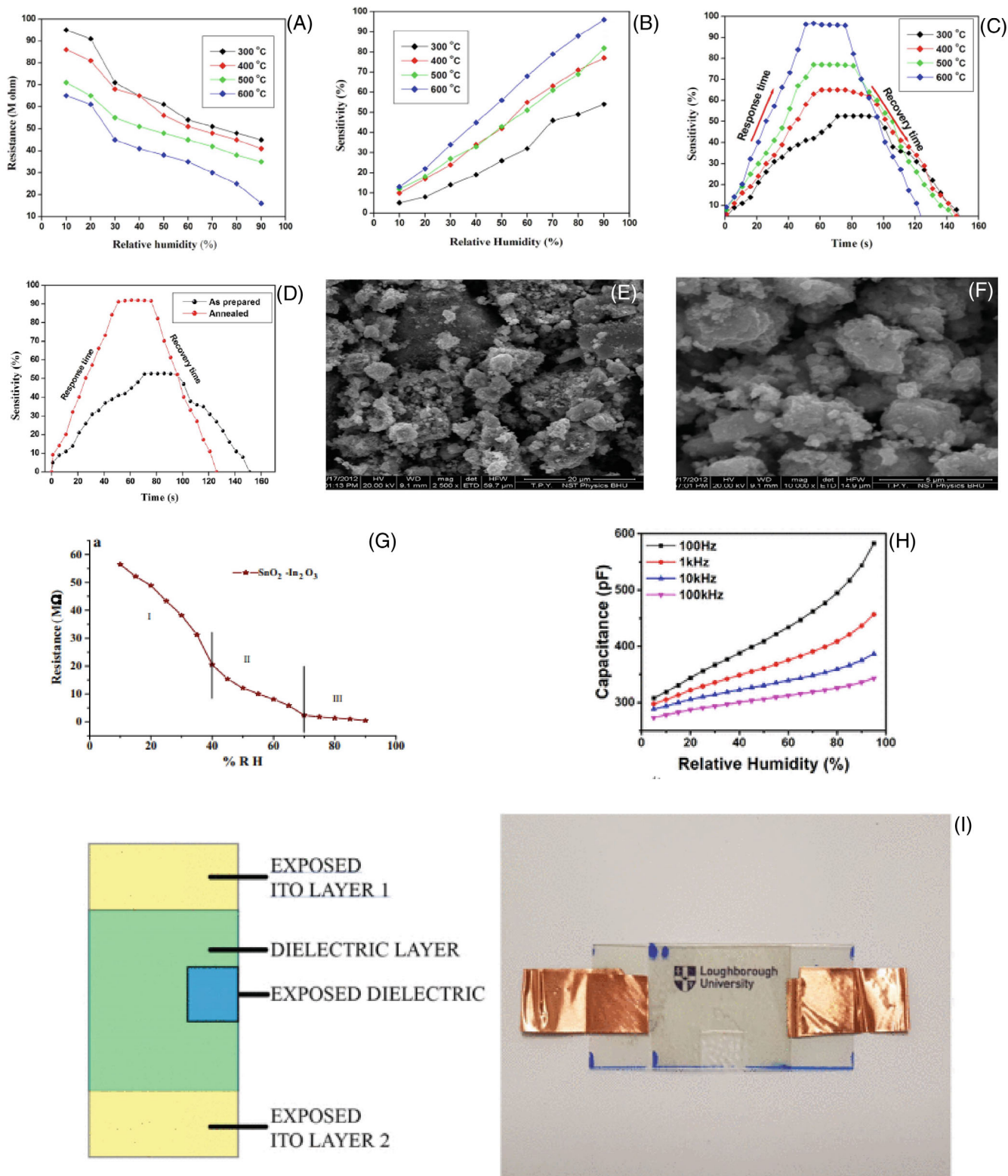


FIGURE 9 (A) Resistance changes on the ITO thin film based on relative humidity (reproduced with permission),³¹ (B) ITO thin film sensitivity with relative humidity (reproduced with permission),³¹ (C) sensitivity response with annealing temperature (reproduced with permission),³¹ (D) the sensitivity of the two different thin film (reproduced with permission),²⁵ (E) SEM magnification of 20 μm (reproduced with permission),³⁴ (F) SEM magnification of 5 μm (reproduced with permission),³⁴ (G) ITO thin film humidity response (reproduced with permission),³⁴ (H) capacitance response based on the frequency measurement (reproduced under the terms of the Creative Commons CC-BY),⁵⁴ and (I) thin film with dielectric ink and copper tape (reproduced under the terms of the CC BY 4.0).¹²

long recovery time due to the dielectric layer requiring more time to reach a saturation level of water absorption, where diffusion was slow.

Table 2 presents various ITO thin films' response and recovery times. The response and recovery times are highly dependent on the preparation method, post processing and humidity level. Particularly, annealing ITO thin films supported quick response and recovery times. The capacitance increased with a rise in relative humidity in all four frequencies because water absorption changed the dielectric properties of alumina. The thin film measured relative humidity from 5% to 95% in a controlled environmental temperature of 25°C. The average thin film sensitivity was 3.8 pF/RH%, 2.11 pF/RH%, 1.185 pF/RH%, 0.85 pF/RH%, and the standard error mean of 0.42 pF/RH%, 0.23 pF/RH%, 0.12 pF/RH%, 0.092 pF/RH% with, respectively, to frequency measurement of 100 Hz, 1 kHz, 10 kHz, and 100 kHz. The thin film's sensitivity decreased with an increase in frequency due to the relative permittivity of the capacitor decreasing with increasing frequency, as shown in Figure 9H. Permittivity was the ability of the electrical storage of thin film, which decreased because the polarization of thin film increased in higher frequency. Analysis showed that the overall percentage of capacitance increased 204%, 164%, 140%, and 127% with 100 Hz, 1 kHz, 10 kHz, and 100 kHz, concerning relative humidity from 5% to 95%. The lower frequency measurement provided a higher response to humidity. Meanwhile, the thin film stability measurement was carried out with four different humidity operation conditions for two different thin film's surface areas. Analysis showed that the long-time humidity absorption on the layer did not damage the coating layer. In lower relative humidity, capacitance on both thin films decreased slightly over 7 days of the stability measurement period. In high relative humidity, capacitance gradually increased with days moving up to 45 days. It is important to note that thin film stability was high and did not show any damage in low and high relative humidity due to metal oxides being naturally highly stable.^{12,54} The results of the experimental studies indicate that the presence of humidity does not affect stability of the ITO thin film. The stability of ITO thin film is considered one of the advantages in designing ITO-based humidity sensor.

TABLE 2 Response and recovery time.

Sr. No	Coating material	Deposition	Response time	Recovery time	Sensitivity	Remarks	References
1	ITO/Alumina	Screen printed process	13.5–26.5 s	4–6 s	0.85–7.76 pF/RH%	Humidity ranges from 5% to 95% (surface area 4 cm ²)	McGhee et al. ⁵⁴
2	ITO/Alumina	Screen printed process	47.2–56 s	49–76.5 s	0.85–7.76 pF/RH%	Humidity ranges from 5% to 95% (surface area 9 cm ²)	McGhee et al. ⁵⁴
3	ITO	Sol-gel spin coating	75 s	65 s	Close to 50%	Relative humidity ranges from 10% to 90%	Babu and Vadivel, ²⁵
4	ITO	Sol-gel spin coating	58 s	45 s	Close to 90%	Relative humidity ranges from 10% to 90% (Annealed at 500°C)	Babu and Vadivel, ²⁵
5	ITO	Sol-gel spin coating	68 s	54 s	Close to 50%	Relative humidity ranges from 10% to 90%	Premkumar and Vadivel, ³¹
6	ITO	Sol-gel spin coating	28 s	14 s	Close to 95%	Relative humidity ranges from 10 to 90% (Annealed at 600°C)	Premkumar and Vadivel, ³¹
7	ITO/dielectric ink	Magnetron sputtering/screen printing	31.5 s	31 s	–	10 Ω/Sq sheet resistance, RH 5–95%	McGhee et al. ^{12,50}
8	PLA-TiO ₂ on ITO plate	Spin coating technique	40 s	20 s	–	RH levels from 70% to 90%	Mallick et al., ¹⁰²

McGhee et al.,¹² deposited ITO thin film on the PET substrate by magnetron sputtering with sheet resistances of 10, 20, and 50 Ω /area (note: the author did not specify the area unit). By screen printing, dielectric ink (D2150901D1) was printed on the surface area of 7.5 cm² of 10 cm² of the ITO thin film. A 1 cm² surface area of wet dielectric removed and exposed dielectric while forming a parallel plate capacitor of ITO thin film. The thin film was cured with a UV lamp (365 nm) for 5 min. The copper tapes were placed on exposed ITO thin film on both sides to enhance electric contact, as shown in Figure 9I. The capacitance was measured through the UNI-T digital LCR meter (with UNI-T digital multi-meter software). The capacitance was measured at three different frequencies of 100 Hz, 1 kHz, and 100 kHz. Analysis showed that the capacitance increased with frequency in the relative humidity range of 5%–95% with all the frequencies, and capacitance behavior was linear with relative humidity above 75% due to water having a larger effect on the dielectric permittivity of thin film. Also, the lower sheet resistance (10 Ω /sq) showed a higher humidity sensitivity with the fixed relative range than the other two thin films. Noteworthy, the stability of thin films gradually changed with an increase in relative humidity (90%) because humidity trapped into the dielectric layer, which changed the dielectric constant and affected the thin film's stability. Also, this thin film was highly suitable for the measurement range of 5% RH to 75% RH.

Sajid et al.¹⁰³ reviewed the progress and future of humidity sensors from a materials perspective. To date, more than many research articles have been published on the development of humidity sensors. The summary includes a comparison of polymers, carbon-based materials, 2D materials, composites, and hybrid materials in relation to humidity sensing. Multidisciplinary and interdisciplinary research on humidity sensors is ongoing, addressing various parameters such as sensitivity, humidity range, response and recovery times, temperature dependence, and cost. Kuzubasoglu¹⁰⁰ provided a mini review of recent studies on humidity sensors. The study summarized a wide range of sensitive materials, including polymer-based, carbon-based, metal oxide-based, and composite-based sensors, and three different humidity sensing mechanisms were elaborated. The ideal specifications for humidity sensing materials included a high response to water vapor, low sensitivity to other gases in the atmosphere, a long operating life, cost-effective technology, high reproducibility, and a uniform and strong binding to the substrate's surface. Oh et al.¹⁰⁴ investigated the mechanical properties of freestanding ITO thin films in relation to annealing. In their investigation, the freestanding ITO thin film was prepared using various sub-steps. As-deposited thin films exhibited an amorphous structure and crystallized at an annealing temperature of 200°C. It was observed that the sheet resistance and transparency changed with the annealing treatment, especially since residual stress was eliminated when the ITO thin film was removed from the confining substrate. Further investigation showed that the young's modulus of the thin film increased with higher annealing temperatures. Analysis showed that the surface morphology was highly dependent on the annealing temperature, and the surface roughness of the ITO thin film decreased from 15.1 to 4.0 nm as the annealing temperature increased. Other mechanical properties such as elongation and tensile strength of the ITO thin film increased up to an annealing temperature of 150°C and then decreased at 200°C. Such behavior was attributed to excessive annealing temperatures leading to intrinsic material failure. Overall, the freestanding investigation method has the advantage of facilitating a better understanding of the intrinsic properties without being affected by residual stress.

Not only are ITO thin films used for humidity and other sensing applications, but ITO-coated substrates and ITO layers have also been employed to enhance the crystal quality and sensing abilities of layers.¹⁰⁵ The properties of advanced materials (i.e., nanomaterial) layers exhibit a strong relationship with the properties of the underlying ITO layer, including surface roughness, crystalline density, and morphology, which were investigated by Alsultany et al.¹⁰⁶ Saleem et al.¹⁰⁷ developed nanostructured multilayer MgF₂ on ITO-coated glass substrates at various substrate temperatures. The surface roughness of the coatings decreased as the substrate temperature increased, resulting in higher optical transmittance at lower substrate temperatures. Additionally, nanostructured multilayer MgF₂/ITO coatings at higher temperatures demonstrated higher shielding effectiveness in the decibel range, making them suitable for aerospace and defense applications. Recently, this study disclosed the enhancement of adhesion strength of ITO thin film by the inkjet printing method. Subsequently, the study investigated the impact of printing parameters on the adhesion strength of thin films. The inkjet procedure was performed at different nozzle speeds and with a different number of layers on a ceramic substrate. Based on the surface morphology results, the adhesion strength of a single layer is much lower than that of two layers and three-layer films due to the very thin and brittle layer being easily damaged under scratch loading. However, multilayers on the substrate increase the internal stress of the substrate, leading to cracks. Furthermore, in inkjet printing, the time gap between each layer and printing speed affects the strength of adhesion.¹⁰⁸

5 | CONCLUDING REMARKS

This review summarizes the preparation of ITO thin films and explores property relationship with annealing temperature, substrate temperature, tin concentration, thin film thickness, and its ability to sense humidity. The acquired knowledge can be utilized for the development of advanced ITO thin films to monitor humidity levels at interfaces. Excessive humidity trapped in single and multilayer interface conditions can lead to significant structural failures. The challenge is inspecting the interface conditions frequently to ensure safe operations. Additionally, using large humidity sensors at interface is not viable, and monitoring conditions away from the interface does not provide real interface humidity conditions. The primary potential application of this ITO thin film preparation and future miniature sized sensor design is to effectively monitor interface conditions in various industrial applications. This review concludes with recommendations for improved ITO thin film preparation parameters to enhance its performance and properties, leading to an efficient sensor design. The selection of deposition method and base materials (e.g., glass, PET) are based on the applications of ITO thin film. Various thin film preparation methods make the ITO thin film for different applications with their own advantages and limitations. Literature shows that spin coating could be more advantageous for thin film preparation than other preparation methods due to its low fabrication cost, ease of preparing the solution, and ability to fabricate large and small thin films. Some properties are independent of the preparation method and synthesis protocols, while other depend on the compound concentration, thickness, and annealing process.

This critical review aims to provide a more suitable ITO thin film preparation method and preparation conditions for developing advanced, highly sensitive thin films for humidity measurement. Limitations and advantages associated with each parameter involved in ITO thin film preparation were discussed. The literature shows that the polymeric substrate could not withstand a high annealing and substrate temperature processes, which is the reason for typically using glass substrates. However, in lower operating temperatures and in certain applications, the polymer substrate could be used as a base material due to its flexibility and low-cost advantages. Also, it has been found that certain range of annealing and substrate temperature improves the properties and humidity sensing performance of the ITO thin film. An annealing temperature of 600°C is suitable for enhancing the crystallinity of thin film with increased grain size. A substrate temperature maximum of 200°C is appropriate for getting an advanced property of thin films. A maximum of 10% of tin concentration is more suitable for the ITO thin film humidity sensing. The thin film's maximum thickness of 300 nm results in lower resistance and good sensitivity. Thin films' properties of low resistivity, high grain size, low bandgap, and polycrystalline structures are fundamental to obtain the highly sensitive ITO humidity sensing thin films. It is important to note that some of the preparation requires special arrangements. For example, the high deposition rate and high substrate temperatures are possible but through certain preparation methods. However, annealing, different tin concentration solutions, and varying thickness can be achieved through almost all the preparation methods.

ITO thin film sense various humidity ranges based on the measurement changes of resistance/capacitance. Thin film humidity sensitivity depends on films properties and humidity range. For example, the low resistance thin film can detect a low humidity level. Band gap minimisation and low thin film thickness enhance conductivity of the thin film and humidity sensitivity. A high humidity range shows a higher conductivity because high water molecules provide many electron donors and reduce thin film resistance. However, it is worth noting that, so far, thin film performance for humidity sensing responses is undetected at humidity levels of 5% relative humidity and below. Overall, the response and recovery times of the humidity sensing are based on the preparation method, conditions, and humidity range.

As a recommendation, ITO thin film could be a more appropriate humidity sensing element to monitor humidity in various conditions with an advanced sensor design of the two parallel plates with pores on one plate to enhance the humidity contact. It is important to consider the properties and limitations of other humidity sensing materials and optimize the range of preparation conditions to develop an improved ITO thin film for efficient humidity sensing. A better ITO humidity sensor should be compact in size to fit into confined spaces (e.g., pipeline-insulation, wall insulation interface) and exhibit improved sensitivity for monitoring even small amount humidity levels and changes. Further experimental study is needed to quantify humidity and sense the relative humidity range below 5%. The outcome of future studies could be to develop a better ITO thin film humidity sensor that can effectively monitor humidity, provide precise humidity change data, and can help with maintenance planning, prevent material failures, and mitigate economic losses.

NOMENCLATURE

- d Crystalline size
- k Shape factor

R_a	Surface height roughness
S	Sensitivity
β	Full width at half maximum
θ	Bragg's angle
λ	Wavelength of the incident beam
ρ	Resistivity
δ	Surface roughness
θ_0	Angle of incidence

AUTHOR CONTRIBUTIONS

Vinooth Rajendran: Conceptualization (lead); investigation (lead); methodology (lead); validation (lead); writing – original draft (lead). **Anil Prathuru:** Methodology (supporting); supervision (supporting); validation (supporting); writing – review and editing (supporting). **Carlos Fernandez:** Methodology (supporting); supervision (supporting); validation (supporting); writing – review and editing (supporting). **Sujatha D:** Methodology (supporting); validation (supporting); writing – review and editing (supporting). **Subhendu K. Panda:** Methodology (supporting); validation (supporting); writing – review and editing (supporting). **Nadimul Haque Faisal:** Conceptualization (lead); funding acquisition (lead); investigation (supporting); methodology (supporting); project administration (lead); supervision (lead); validation (supporting); writing – review and editing (supporting).

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CONFLICT OF INTEREST STATEMENT

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PEER REVIEW

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DATA AVAILABILITY STATEMENT

Data sharing is not applicable to this article as no new data were created or analyzed in this study.

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