ROBERT GORDON UNIVERSITY ABERDEEN

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TITLE:
Comprehensive steady state analysis of bidirectional dual active bridge DC/DC converter using triple phase shift control.

## YEAR:

Publisher citation:
HARRYE, Y.A., AHMED, K.H., ADAM, G.P. and ABOUSHADY, A.A. 2014. Comprehensive steady state analysis of bidirectional dual active bridge DC/DC converter using triple phase shift control. In the Proceedings of the 23rd International symposium on industrial electronics (ISIE 2014), 1-4 June 2014, Istanbul, Turkey. New York: IEEE [online], pages 437-442. Available from: https://dx.doi.org/10.1109/ISIE.2014.6864653

HARRYE, Y.A., AHMED, K.H., ADAM, G.P. and ABOUSHADY, A.A. 2014. Comprehensive steady state analysis of bidirectional dual active bridge DC/DC converter using triple phase shift control. In the Proceedings of the 23rd International symposium on industrial electronics (ISIE 2014), 1-4 June 2014, Istanbul, Turkey. New York: IEEE, pages 437-442. Held on OpenAIR [online]. Available from: https://openair.rgu.ac.uk

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and presented at the Proceedings of the 23rd International symposium on industrial electronics (ISIE 2014), 1-4 June 2014, Instanbul, Turkey.
(ISBN $\qquad$ ; eISBN $\qquad$ ; ISSN 2163-5145 _).
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# Comprehensive Steady State Analysis of Bidirectional Dual Active Bridge DC/DC Converter Using Triple Phase Shift Control 

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#### Abstract

Several papers have been published recently on TPS control of dual active bridge (DAB) converter, however, no complete study of the converter operation behaviour exists, that takes into account all switching modes in both charging and discharging (bidirectional) power transfer. In this paper, six switching modes and their complements with opposite power transfer direction are defined with their operational constraints. Exact expressions for power transferred are derived with no fundamental frequency assumptions and range of power transfer for each mode is also defined to characterize mode limitations. Detailed constraints for zero voltage switching (ZVS) are also obtained. A new definition for converter reactive power consumption is introduced. This is based on calculation of inductor apparent power which avoids fundamental frequency approximations as well as the vague negative (back flowing) power definitions in recent papers. All known DAB phase shift modulation techniques including conventional, dual and extended phase shift, represent special cases from triple phase shift, therefore the presented analysis provides a generalised theory for all phase shift based modulation techniques.


Keywords- Dual active bridge converter (DAB), Reactive power, Triple phase shift (TPS), Zero voltage switching (ZVS)

## I. InTRODUCTION

There has been considerable interest in the development and applications of bidirectional dual active bridge converter (DAB) in recent years because of its use in existing and emerging power electronics technologies. This includes renewable energy generation, energy storage, electric vehicles, power distribution in microgrids and aircraft power distribution. Renewable energy generation and its use have been on the rise and among these prime sources is the offshore wind power generation. The most formidable challenges of this technology have been the interconnection and integration of different wind farm clusters, requiring use of dc connections mechanism [1]. Also owing to the unpredictable peak power as a result of wind speed fluctuations, energy storage means are required to store energy during high generation peaks and retrieve during low peaks. Therefore in order to transmit power from offshore wind farms to onshore for regulation, interface and integrate in a future DC grid; high


Fig. 1 DAB circuit diagram
power dc-dc converters are required as the main interface component.
Isolated dual active bridge (DAB) bidirectional converter which was proposed in [2], and is illustrated in Fig.1, has shown to be attractive candidate for high power $\mathrm{dc} / \mathrm{dc}$ conversion due its high power density, soft switching capability, bidirectional power flow, galvanic isolation and simple control methods [2]. Use of DAB with 1MW renewable offshore wind energy has been shown [3]. Investigation of DAB converter for electric vehicles and in energy storage system has been shown to be promising [4]

Studies have been on going to analyse, design, control and improve the overall performance of DAB converter. Among these are the modulation schemes [5-7], converter losses reduction [8-10] and modelling of the converter characteristics [11-12]. The first proposed conventional phase shift (CPS) modulation scheme [13] operates the converter bridges with full square waves while controlling the phase shift angle between them in order to determine power transferred. However, it has limitations of high inductor RMS current and hard switching at light loads under wide variations of the input and output voltages. Therefore, different modulations techniques have been published to solve aforementioned problems [5].
Dual phase shift (DPS) modulation [6] aims to reduce the converter reactive power, increase overall efficiency and during power up, suppress the transformer inrush current but has a limitation of some suboptimal modes [7]. Pulse width modulations (PWM) control of DAB converter has been
investigated in detail by [7], whereby using hybrid combinations of CPS, single PWM (SPWM) and dual PWM (DPWM), the authors have shown an improvement in low power operation of the converter in wide input output voltages, extension of ZVS range, reduction of peak and RMS currents and further reduction of the transformer size. In [8], DAB converter using PWM modulation was introduced and shown an extension of ZVS. Triple phase shift modulation was studied by [7, 14-17] to further enhance the performance of the converter in terms of improving ZVS range and reducing the overall losses by introducing an extra control variable. In [14], TPS was introduced to study the stability of DAB converter. In [15], only four TPS modes were studied to extend ZVS operating range and increase the converter efficiency. Twelve switching modes were identified by [9, 16] and analysis was performed only for some of the modes using fundamental component approximation. In [17], a multiphase shift scheme including DPS and TPS was investigated to determine optimal sub modes from the waveform features.
In this paper a comprehensive investigation of DAB converter operation is addressed. Although recently several papers have been published on TPS, complete switching modes analysis of the converter with the operational constraints has not been studied. All the operating modes are investigated with their operational constraints while deriving exact expressions for inductor currents at each switching instant. Average power transferred is calculated without using fundamental frequency approximation. Moreover, ZVS possibilities and detailed constraints for the converter bridges are determined. Reactive power, unlike recent papers defining it as back-flowing power [6, 19, 20], and papers assuming fundamental frequency analysis [21] is defined and calculated on a more accurate basis. This involves calculation of nonsinusoidal apparent power at the inductor. The analysis presented in this paper provides a generalised theory for all phase shift modulation methods since they are all a special case of TPS. This will be discussed in section four.

## II. DAB TRIPLE PHASE SHIFT CONTROL

By referring the converter in Fig. 1 to the transformer primary side, neglecting transformer magnetizing inductance and assuming $n: 1$ turns ratio, DAB converter can be simplified to equivalent circuit model in Fig. 2 (a).
In TPS, three control parameters $\mathrm{D}_{1}, \mathrm{D}_{2}$ and $\mathrm{D}_{3}$ are used. $\mathrm{D}_{1}$ is the phase shift between switches $S_{1} \& S_{4}, D_{2}$ is the phase shift between the switches $\mathrm{Q}_{1} \& \mathrm{Q}_{4}$ while $\mathrm{D}_{3}$ is the phase shift between $S_{1} \& Q_{1}$ as illustrated in Fig. 2 (b).
Analysis is performed assuming lossless converter and transformer with $V_{1}>n V_{2}$, i.e buck mode operation. Both power flow directions (charging \& discharging in case $D A B$ is connected to a battery) are studied. Analysis of boost mode operation ( $V_{1}<n V_{2}$ ) is identical to buck mode and will be omitted in this paper.
By referring to Fig. 2(a), the instantaneous inductor current $i_{L}(t)$, can be calculated using


Fig.2. (a) DAB equivalent circuit referred to transformer primary side (b) voltage and currents waveforms for TPS control

$$
\begin{equation*}
i_{L}(t)=\frac{1}{L} \int_{t_{n}}^{t}\left(v_{p}(t)-v_{s}^{\prime}(t)\right) d t+i_{L}\left(t_{n}\right) \quad t_{n} \leq t \leq t_{n+1} \tag{1}
\end{equation*}
$$

where $t_{n}$ represents $n^{\text {th }}$ switching instant as illustrated in Fig.2(b).

Inductor currents are crucial for active and reactive power calculations, in addition to determination of ZVS constraints. This will be discussed further in section three.

## III. PERFORMANCE ANALYSIS

Based on combinations of phase shifts $D_{1}, D_{2}$ and $D_{3}$, full, partial and no overlaps of both bridges quasi square wave voltages give rise to six switching modes and their complements, for both power flow directions using TPS. Typical operating waveforms for each of these modes are illustrated in Table I.
In order to assess the modes of operation, several performance indicators of DAB converter are investigated. Exact expressions for the inductor current are needed. Normalised inductor currents to $\left(1 / 4 f_{s} L\right)$, for the positive half cycle switching instants are derived and listed in Table II (where $f_{s}$ is the switching frequency). Due to inductor current half-wave symmetry, negative half cycle values are omitted. These can be calculated by negating their counter positive half cycle values.

## A. Power Transfer

Average power transferred by the converter can be calculated at either bridge by assuming a lossless inductor using

$$
\begin{equation*}
P=\frac{1}{T_{h}} \int_{0}^{T_{h}} v_{p}(t) i_{L} d t=\frac{1}{T_{h}} \int_{0}^{T_{h}} v_{s}^{\prime}(t) i_{L} d t \tag{2}
\end{equation*}
$$

Per unit expression for average power normalised with respect to maximum possible transferable power by the converter is shown in Table I for each mode. Maximum power (used as the base power), is obtained with CPS at $90^{\circ}$ phase shift between bridge voltages [9, 13].

$$
\begin{equation*}
P_{\text {base }}=\frac{n V_{1} V_{2}}{8 f_{s} L} \tag{3}
\end{equation*}
$$

Table I: DAB modes of operation \& power equations using TPS control

|  | Mode 1 | Mode 1' | Mode 2 | Mode 2' |
| :---: | :---: | :---: | :---: | :---: |
| Waveforms |  | 1 |  |  |
|  | $t_{o}=0, t_{1}=D_{3} T_{h}, t_{2}=\left(D_{3}+D_{2}\right) T_{h}, t_{3}=D_{1} T_{h}, t_{4}=T_{h}=\frac{1}{2 f_{s}}$ |  | $t_{0}=0, t_{1}=D_{1} T_{h}, t_{2}=\left(D_{2}+D_{3}-1\right) T_{h}, t_{3}=D_{3} T_{h}, t_{4}=T_{h}=\frac{1}{2 f_{s}}$ |  |
| Mode operational constraints | $\begin{aligned} & D_{1} \geq D_{2} \\ & 0 \leq D_{3} \leq D_{1}-D_{2} \end{aligned}$ |  | $\begin{aligned} D_{2} & \geq D_{1} \\ \left(1+D_{1}-D_{2}\right) & \leq D_{3} \leq 1 \end{aligned}$ |  |
| Power \& power range (pu) | $P_{p u}=2\left[D_{2}^{2}-D_{1} D_{2}+2 D_{2} D_{3}\right]$ <br> Range : $P_{\max }=0.5 p u, P_{\min }=-0.5 p u$ | $\begin{aligned} & P_{p u}=-2\left[D_{2}{ }^{2}-D_{1} D_{2}+2 D_{2} D_{3}\right] \\ & \text { Range }: P_{\max }=0.5 p u, P_{\min }=-0.5 p u \end{aligned}$ | $\begin{aligned} & P_{p u}=2\left[D_{1}^{2}-D_{1} D_{2}+2 D_{1}-2 D_{1} D_{3}\right] \\ & \text { Range }: P_{\max }=0.5 p u, P_{\min }=-0.5 p u \end{aligned}$ | $\begin{aligned} & P_{p u}=-2\left[D_{1}^{2}-D_{1} D_{2}+2 D_{1}-2 D_{1} D_{3}\right] \\ & \text { Range }: P_{\max }=0.5 p u, P_{\min }=-0.5 p u \end{aligned}$ |
|  | Mode 3 | Mode 3' | Mode 4 | Mode 4' |
| Waveforms | $S_{1} S_{4} S_{2} S_{4}^{4} S_{2} S_{4} S_{2} S_{4} S_{2} S_{3} S_{1} S_{3} S_{1} S_{3} S_{1} S_{3}$ $\mathbf{Q}_{1} \mathbf{Q}_{3} \mathbf{Q}_{1} \mathbf{Q}_{3} \mathbf{Q}_{1} \mathbf{Q}_{4} \mathbf{Q}_{2} \mathbf{Q}_{4} \mathbf{Q}_{2} \mathbf{Q}_{4} \mathbf{Q}_{2} \mathbf{Q}_{4} \mathbf{Q}_{2} \mathbf{Q}_{3} \mathbf{Q}_{1} \mathbf{Q}_{3}$ |  <br> $t_{0} \quad t_{1} t_{2} t_{3} \quad t_{4} \quad t_{5} t_{6} t_{7} t_{8}$ <br> $s_{1} S_{4}^{4} S_{2} S_{4} s_{2} S_{4} S_{2} S_{4} S_{2} S_{3} S_{1} S_{3} S_{1} S_{3} s_{1} s_{3}$ $\mathbf{Q}_{2} \mathbf{Q}_{4} \mathbf{Q}_{2} \mathbf{Q}_{4} \mathbf{Q}_{2} \mathbf{Q}_{3} \mathbf{Q}_{1} \mathbf{Q}_{3} \mathbf{Q}_{1} \mathbf{Q}_{3} \mathbf{Q}_{1} \mathbf{Q}_{3} \mathbf{Q}_{1} \mathbf{Q}_{4} \mathbf{Q}_{2} \mathbf{Q}_{4}$ |  |  |
|  | $t_{o}=0, t_{1}=D_{1} T_{h}, t_{2}=D_{3} T_{h}, t_{3}=\left(D_{2}+D_{3}\right) T_{h}, t_{4}=T_{h}=\frac{1}{2 f_{s}}$ |  | $t_{o}=0, t_{1}=\left(D_{2}+D_{3}-1\right) T_{h}, t_{2}=D_{1} T_{h}, t_{3}=D_{3} T_{h}, t_{4}=T_{h}=\frac{1}{2 f_{s}}$ |  |
| Mode operational constraints | $\begin{aligned} & D_{2} \leq 1-D_{1} \\ & D_{1} \leq D_{3} \leq 1-D_{2} \end{aligned}$ |  | $\begin{aligned} D_{1} & \leq D_{3} \leq 1 \\ 1-D_{3} & \leq D_{2} \leq 1-D_{3}+D_{1} \end{aligned}$ |  |
| Power \& power range (pu) | $\begin{aligned} & \quad P_{p u}=2\left[D_{1} D_{2}\right] \\ & \text { Range }: P_{\max }=0.5 p u, P_{\min }=0 p u \end{aligned}$ | $P_{p u}=-2\left[D_{1} D_{2}\right]$ <br> Range: $P_{\max }=0.0 p u, P_{\min }=-0.5 p u$ | $P_{p u}=2\left[\begin{array}{l} -D_{2}^{2}-D_{3}^{2}+2 D_{2}+2 D_{3} \\ -2 D_{2} D_{3}+D_{1} D_{2}-1 \end{array}\right]$ <br> Range : $P_{\max }=0.67 p u, P_{\min }=0 p u$ | $\begin{aligned} & P_{p u}=-2\left[\begin{array}{l} -D_{2}^{2}-D_{3}^{2}+2 D_{2}+2 D_{3} \\ -2 D_{2} D_{3}+D_{1} D_{2}-1 \end{array}\right] \\ & \text { Range }: P_{\max }=0 p u, P_{\min }=-0.67 p u \end{aligned}$ |
|  | Mode 5 | Mode 5' | Mode 6 | Mode 6' |
| Waveforms |  | $\begin{array}{llllll} D_{1} D_{1} T_{h} \\ \hdashline V_{1} & & i_{L}(t) \\ \hline \end{array}$ |  |  |
|  | $t_{o}=0, t_{1}=D_{3} T_{h}, t_{2}=D_{1} T_{h}, t_{3}=\left(D_{2}+D_{3}\right) T_{h}, t_{4}=T_{h}=\frac{1}{2 f_{s}}$ |  | $t_{o}=0, t_{1}=\left(D_{2}+D_{3}-1\right) T_{h}, t_{2}=D_{3} T_{h}, t_{3}=D_{1} T_{h}, t_{4}=T_{h}=\frac{1}{2 f_{s}}$ |  |
| Mode operational constraints | $\begin{aligned} D_{1}-D_{3} & \leq D_{2} \leq 1-D_{3} \\ 0 & \leq D_{3} \leq D_{1} \end{aligned}$ |  | $\begin{aligned} & 1-D_{2} \leq D_{1} \\ & 1-D_{2} \leq D_{3} \leq D_{1} \end{aligned}$ |  |
| Power \& power range (pu) | $\begin{aligned} & P_{p u}=2\left[-D_{1}^{2}-D_{3}^{2}+D_{2} D_{1}+2 D_{1} D_{3}\right] \\ & \text { Range }: P_{\max }=0.667 p u, P_{\min }=0 p u \end{aligned}$ | $\begin{aligned} & P_{p u}=-2\left[-D_{1}^{2}-D_{3}^{2}+D_{2} D_{1}+2 D_{1} D_{3}\right] \\ & \text { Range }: P_{\max }=0 p u, P_{\min }=-0.667 p u \end{aligned}$ | $\begin{aligned} & P_{p u}=2\left[\begin{array}{l} -D_{1}^{2}-D_{2}^{2}-2 D_{3}^{2}+2 D_{3}-2 D_{2} D_{3} \\ +D_{1} D_{2}+2 D_{1} D_{3}+2 D_{2}-1 \end{array}\right] \\ & \text { Range: } P_{\max }=1 p u, P_{\min }=0 p u \end{aligned}$ | $\begin{aligned} & P_{p u}=-2\left[\begin{array}{l} -D_{1}^{2}-D_{2}^{2}-2 D_{3}^{2}+2 D_{3}-2 D_{2} D_{3} \\ +D_{1} D_{2}+2 D_{1} D_{3}+2 D_{2}-1 \end{array}\right] \\ & \text { Range: } P_{\max }=0 p u, P_{\min }=-1 p u \end{aligned}$ |

Range of power transfer for each mode is also computed to characterise mode limitations. This is done by applying the mode operational constraints to the derived power equations. The result gives the capability of converter power transfer, with respect to the full range under specific mode of operation In complementary modes, bridge 2 waveform is shifted by $180^{\circ}$ from original mode, hence resulting in the exact but negative power range. From Table I, it can be seen that modes 6 and 6' are the only modes that cover the whole converter operating power range. Modes 1, 1', 2 and 2'are capable of charging and discharging operation, but only for half the power range. Modes 3, 4, 5 and 6 with their complements only provide unidirectional power transfer capability each. Finally, it is worth noting that, in mode 3 and 3 ' power transfer is independent of $\mathrm{D}_{3}$, meaning that it can be solely controlled by controlling bridge voltages.

## B. ZVS Constraints

To ensure that the converter switches are operating with ZVS, anti-parallel diodes must conduct prior to switch turn on. After switch voltage drops to zero, current commutates from the anti-parallel diode to the switch enabling turn on at zero voltage, resulting in zero turn on power loss. This is a desirable converter characteristic since it enables its use in high power applications in addition to a more compact design due to use of higher switching frequency and lower electromagnetic interference.
Therefore, at the instant of switch turn on, current in the switch must be negative. This provides the condition for ZVS. With respect to the switches in Fig. 1 and their respective conducting current directions, conditions for the switches ZVS can be summarised as

$$
\left.\begin{array}{ll}
\text { For } S_{1}, S_{4}, Q_{2}, Q_{3} & \left.i_{L}\right|_{t=\text { turnon }}<0 \\
\text { For } & S_{2}, S_{3}, Q_{1}, Q_{4} \tag{4}
\end{array} i_{L}\right|_{t=\text { turnon }}>0 ~ \$ ~ l
$$

According to (4), ZVS constraints are derived and listed in Table III. Only modes where ZVS is realisable for all switches are indicated with their constraints. Otherwise ZVS is only partially obtained for the converter, i.e. for some switches only as indicated by modes $1,3,3$ ' and 5 .

## C. Reactive power

Reactive power is a parameter of interest to investigate in DAB converter, since by reducing reactive power consumption; RMS inductor current is minimised for a specified level of power transfer. This reduces conduction and copper losses. Reactive power loss in DAB converter has been discussed in literature [6, 18-20]. In [6, 19], authors attempt to reduce reactive power loss by introducing dual phase shift control. Unlike CPS, the transformer primary and secondary voltages are not limited to $50 \%$ duty cycles but zero states are introduced, resulting in a quasi-square waveform. In [20], reactive power is defined as back-flowing power to the converter. This is inaccurate because it means that reactive


Fig. 3 Inductor voltage and current waveforms for mode 6
power only occurs when voltage and current are of opposite polarity (negative power flow). So if no negative power flow exists, then converter has zero reactive power consumption. However, this is in accurate since DAB always absorbs reactive power from the converters due to coupling inductance no matter the level of power transfer. In [18], authors propose a method to minimise reactive power through design of optimal transformer turns ratio.
In this paper, a new definition of reactive power is introduced based on calculating apparent power $S_{L}$ at the inductor. Since inductors do not absorb active power, the apparent power is therefore equivalent to the reactive power consumption by the inductor. This is, by definition, equivalent to the total reactive power consumption at the converter bridges. Reactive power $Q$, can be defined by

$$
\begin{equation*}
Q=S_{L}=V_{L_{R M S}} I_{L_{R M S}} \tag{5}
\end{equation*}
$$

For each mode, RMS values for the inductor voltage and current are calculated. Fig. 3 illustrates typical waveforms for $v_{L}$ and $i_{L}$ for mode 6. Reactive power definitions are given in Table III for all modes.

## IV. TPS ANALYSIS GENERALISATION

The analysis presented in this paper can be generalised for all known phase shift modulation techniques. To illustrate this, the following examples are considered by applying the mode operational constraints defined in Table I.

- Conventional phase shift (CPS) [13]: This is defined by $D_{1}=1, D_{2}=1$. Applying this definition to modes 6 and 6' yields $0 \leq D_{3} \leq 1$. CPS is therefore fully achieved with these two modes.
- Dual phase shift (DPS) [6, 19]: This is characterized by $D_{1}=D_{2}=D$. Applying this definition to modes 6 and 6' yields $D \geq 0.5$ and $1-D \leq D_{3} \leq D$. This does not represent the complete control range for $D$.
Considering modes 3 and $3^{\prime}$, if $D_{1}=D_{2}=D$, therefore $D \leq 0.5$ and $D \leq D_{3} \leq 1-D$. Consequently, modes 3, 3', 6 and 6' can cover the whole operating range for DPS.
- Extended phase shift (EPS) [20]: This is defined by either bridge voltages being a full square wave with the other bridge voltage controlled to be a quasi-square wave. Considering modes 6 and ${ }^{\prime}$ ', if

$$
\text { - } D_{1}=1 \text {, therefore } D_{2} \geq 0 \text { and } 1-D_{2} \leq D_{3} \leq 1 \text {. }
$$

Table II: Inductor currents ( $i_{L}$ ) for positive half cycle switching intervals normalized to $1 / 4 f_{s} L$.

| Modes | $\boldsymbol{i}_{L}\left(t_{0}\right)$ | $\boldsymbol{i}_{L}\left(\boldsymbol{t}_{1}\right)$ | $\boldsymbol{i}_{L}\left(\boldsymbol{t}_{2}\right)$ | $i_{L}\left(t_{3}\right)$ |
| :---: | :---: | :---: | :---: | :---: |
| 1 | $-\left[V_{1} D_{1}-n V_{2} D_{2}\right]$ | $\left[-V_{1} D_{1}+2 V_{1} D_{3}+n V_{2} D_{2}\right]$ | $\left[-V_{1} D_{1}+2 V_{1} D_{2}+2 V_{1} D_{3}-n V_{2} D_{2}\right]$ | $\left[V_{1} D_{1}-n V_{2} D_{2}\right]$ |
| 1 ' | $-\left[V_{1} D_{1}+n V_{2} D_{2}\right]$ | $\left[-V_{1} D_{1}+2 V_{1} D_{3}-n V_{2} D_{2}\right]$ | $\left[-V_{1} D_{1}+2 V_{1} D_{2}+2 V_{1} D_{3}+n V_{2} D_{2}\right]$ | $\left[V_{1} D_{1}+n V_{2} D_{2}\right]$ |
| 2 | $-\left[V_{1} D_{1}-2 n V_{2}+n V_{2} D_{2}+2 n V_{2} D_{3}\right]$ | $\left[V_{1} D_{1}+2 n V_{2} D_{1}-n V_{2} D_{2}+2 n V_{2}-2 n V_{2} D_{3}\right]$ | $\left[V_{1} D_{1}+n V_{2} D_{2}\right]$ | $\left[V_{1} D_{1}+n V_{2} D_{2}\right]$ |
| 2 ' | $-\left[V_{1} D_{1}+2 n V_{2}-n V_{2} D_{2}-2 n V_{2} D_{3}\right]$ | $\left[V_{1} D_{1}-2 n V_{2}-2 n V_{2} D_{1}+n V_{2} D_{2}+2 n V_{2} D_{3}\right]$ | $\left[V_{1} D_{1}-n V_{2} D_{2}\right]$ | $\left[V_{1} D_{1}-n V_{2} D_{2}\right]$ |
| 3 | $-\left[V_{1} D_{1}-n V_{2} D_{2}\right]$ | $\left[V_{1} D_{1}+n V_{2} D_{2}\right]$ | $\left[V_{1} D_{1}+n V_{2} D_{2}\right]$ | $\left[V_{1} D_{1}-n V_{2} D_{2}\right]$ |
| 3 ' | $-\left[V_{1} D_{1}+n V_{2} D_{2}\right]$ | $\left[V_{1} D_{1}-n V_{2} D_{2}\right]$ | $\left[V_{1} D_{1}-n V_{2} D_{2}\right]$ | $\left[V_{1} D_{1}+n V_{2} D_{2}\right]$ |
| 4 | $-\left[V_{1} D_{1}-2 n V_{2}+n V_{2} D_{2}+2 n V_{2} D_{3}\right]$ | $\left[-V_{1} D_{1}-2 V_{1}+2 V_{1} D_{2}+n V_{2} D_{2}+2 V_{1} D_{3}\right]$ | $\left[V_{1} D_{1}+n V_{2} D_{2}\right]$ | $\left[V_{1} D_{1}+n V_{2} D_{2}\right]$ |
| 4' | $-\left[V_{1} D_{1}+2 n V_{2}-n V_{2} D_{2}-2 n V_{2} D_{3}\right]$ | $\left[-V_{1} D_{1}-2 V_{1}+2 V_{1} D_{2}+2 V_{1} D_{3}-n V_{2} D_{2}\right]$ | $\left[V_{1} D_{1}-n V_{2} D_{2}\right]$ | $\left[V_{1} D_{1}-n V_{2} D_{2}\right]$ |
| 5 | $-\left[V_{1} D_{1}-n V_{2} D_{2}\right]$ | $\left[-V_{1} D_{1}+2 V_{1} D_{3}+n V_{2} D_{2}\right]$ | $\left[V_{1} D_{1}-2 n V_{2} D_{1}+n V_{2} D_{2}+2 n V_{2} D_{3}\right]$ | $\left[V_{1} D_{1}-n V_{2} D_{2}\right]$ |
| $5 ’$ | $-\left[V_{1} D_{1}+n V_{2} D_{2}\right]$ | $\left[-V_{1} D_{1}+2 V_{1} D_{3}-n V_{2} D_{2}\right]$ | $\left[V_{1} D_{1}+2 n V_{2} D_{1}-n V_{2} D_{2}-2 n V_{2} D_{3}\right]$ | $\left[V_{1} D_{1}+n V_{2} D_{2}\right]$ |
| 6 | $-\left[V_{1} D_{1}+n V_{2} D_{2}+2 n V_{2} D_{3}-2 n V_{2}\right]$ | $\left[-V_{1} D_{1}+2 V_{1} D_{2}+2 V_{1} D_{3}+n V_{2} D_{2}-2 V_{1}\right]$ | $\left[-V_{1} D_{1}+2 V_{1} D_{3}+n V_{2} D_{2}\right]$ | $\left[V_{1} D_{1}-2 n V_{2} D_{1}+n V_{2} D_{2}+2 n V_{2} D_{3}\right]$ |
| 6 , | $-\left[V_{1} D_{1}-n V_{2} D_{2}-2 n V_{2} D_{3}+2 n V_{2}\right]$ | $\left[-V_{1} D_{1}+2 V_{1} D_{2}+2 V_{1} D_{3}-n V_{2} D_{2}-2 V_{1}\right]$ | $\left[-V_{1} D_{1}+2 V_{1} D_{3}-n V_{2} D_{2}\right]$ | $\left[V_{1} D_{1}+2 n V_{2} D_{1}-n V_{2} D_{2}-2 n V_{2} D_{3}\right]$ |

Table III: Constraints for operation of all switches under ZVS in the whole operating range \& reactive power expressions.

|  | Mode 1 | Mode 1' | Mode 2 | Mode 2' |
| :---: | :---: | :---: | :---: | :---: |
| ZVS possible for all switches? Constraints | NO | YES $i_{L}\left(t_{0}\right)<0, i_{L}\left(t_{1}\right)<0 \& i_{L}\left(t_{2}\right)>0$ | YES $i_{L}\left(t_{0}\right)<0, i_{L}\left(t_{1}\right)>0 \& i_{L}\left(t_{2}\right)>0$ | YES $i_{L}\left(t_{0}\right)<0, i_{L}\left(t_{1}\right)>0 \& i_{L}\left(t_{2}\right)<0$ |
| Reactive power (Q) |  |  |  |  |
|  | Mode 3 | Mode 3' | Mode 4 | Mode 4' |
| ZVS possible for all switches? Constraints | NO | NO | YES $i_{L}\left(t_{0}\right)<0, i_{L}\left(t_{1}\right)>0 \& i_{L}\left(t_{2}\right)>0$ | NO |
| Reactive power (Q) |  |  |  |  |
|  | Mode 5 | Mode 5' | Mode 6 | Mode 6' |
| ZVS possible for all switches? Constraints | NO | $\begin{gathered} \text { YES } \\ i_{L}\left(t_{0}\right)<0, i_{L}\left(t_{1}\right)<0 \& i_{L}\left(t_{2}\right)>0 \end{gathered}$ | YES $i_{L}\left(t_{0}\right)<0, i_{L}\left(t_{1}\right)>0, i_{L}\left(t_{2}\right)>0 \& i_{L}\left(t_{3}\right)>0$ | YES $i_{L}\left(t_{0}\right)<0, i_{L}\left(t_{1}\right)<0, i_{L}\left(t_{2}\right)<0 \& i_{L}\left(t_{3}\right)>0$ |
| Reactive power (Q) |  |  |  |  |

$$
\text { - } D_{2}=1 \text {, therefore } D_{1} \geq 0 \text { and } 0 \leq D_{3} \leq D_{1} \text {. }
$$

EPS can therefore partially be achieved with modes 6 and $6^{\prime}$. Modes $1,1^{\prime}, 2$ and $2^{\prime}$ cover the remaining EPS range of operation.
For Modes 1 and 1', if:

$$
\text { - } \quad D_{1}=1 \text {, therefore } D_{2} \leq 1 \text { and } 0 \leq D_{3} \leq 1-D_{2} .
$$

For Modes 2 and 2', if:

$$
\text { - } \quad D_{2}=1 \text {, therefore } D_{1} \leq 1 \text { and } D_{1} \leq D_{3} \leq 1
$$

## V. SUMMARY AND CONCLUSIONS

Analysis of DAB converter using TPS was presented in this paper. Six switching modes and their complements have been identified using TPS control. Rather than using fundamental component approximation to determine average power transmitted, detailed analytical investigation was performed on all the switching modes. Moreover, based on the mode constraint, the power transfer range for each mode was derived. Inductor current which is crucial in the determination of DAB performances indices was computed for all the modes at each time intervals. ZVS constraints have been derived for all the modes in terms of possibility for all converter switches that can possibly operate under ZVS in different modes. A new accurate definition for DAB reactive power consumption is introduced based on the non-sinusoidal apparent power at the inductor. It is finally proven that the presented TPS analysis can be generalised to include all phase shift modulation techniques.

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